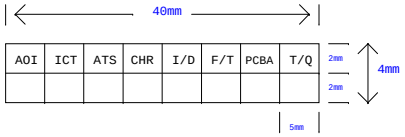
V10IL1 Schematics Rev:C

V10ILX M/B Ver:C
P/N:37GV10000-C0
Made in China

PAGE CONTENT



MB	Revision History	
A	02/20/09	Initial REV.A
A1	03/12/09	Release REV.A1
B	04/09/09	Release REV.B
C	??/??/09	Release REV.C

DB	Revision History	
A	02/19/09	Initial REV.A
B	04/08/09	Release REV.B
C	??/??/09	Release REV.C

V10ILX P/N LIST

	PCB P/N	PCB ASSY P/N
PCB M/B BD FOR V10IL1 REV.A	37GV10000-A0	82GV10000-A0
PCB M/B BD FOR V10IL1 REV.B	37GV10000-B0	82GV10000-B0
		82GV10010-B0 (W/O SIM)
		82GV10030-B0 (N280)
		82GV10050-B0 (W/O SIM& MODEM)
PCB M/B BD V10IL1 R:C	37GV10000-C0	82GV10000-C0
		82GV10010-C0 (W/O SIM)
		82GV10030-C0 (N280)

V10ILX DB REV:A P/N LIST

	PCB P/N	PCB ASSY P/N
PCB 3G Small BD FOR V10IL1 REV.A	35GJV1000-A0	80GJV1000-A0
PCB IO BD FOR V10IL1 REV.A	35GNV1000-A0	80G3V1000-A0
PCB PWR SW BD FOR V10IL1 REV:A	35G0V1000-A0	80G0V1000-A0
PCB 3G Small BD FOR V10IL1 REV.B	35GJV1000-B0	80GJV1000-B0
		80GJV10A0-B0 (W/O 3G)
PCB IO BD FOR V10IL1 REV.B	35G3V1000-B0	80G3V1000-B0
		80G3V10A0-B0 (W/O RJ11)
PCB 3G/MDC BD V10IL1 R:C	35GJV1000-C0	80GJV1000-C0
		80GJV10A0-C0 (W/O 3G)
PCB IO BD V10IL1 R:C	35G3V1000-C0	80G3V1000-C0
		80G3V10A0-C0 (W/O RJ11)

ECS ELITEGROUP
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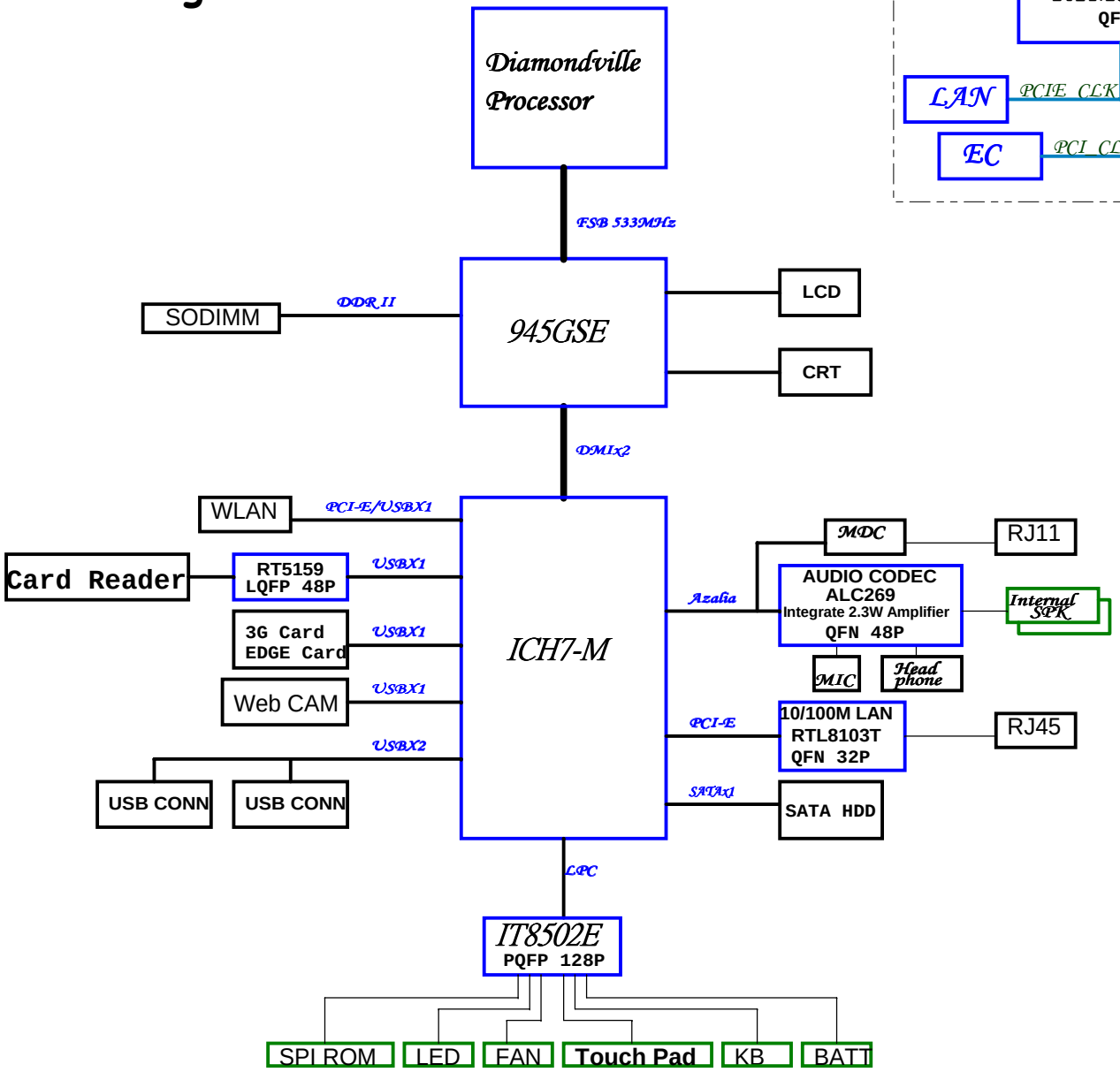
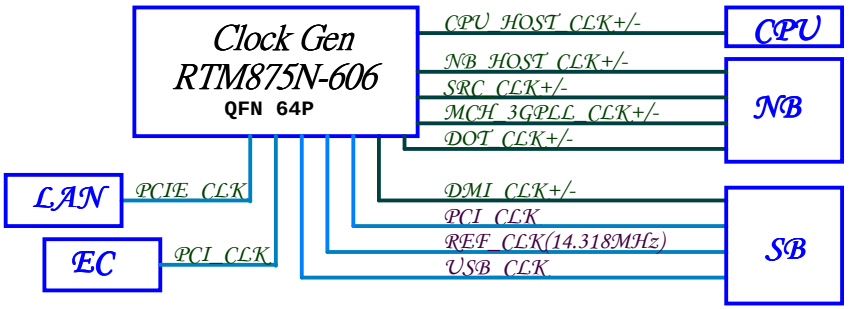
V10II1
System Block Diagram



Clock delivery Block



ECS ELITEGROUP

Project : V10IL1

Size	Document Number	Rev
	System Block Diagram	B

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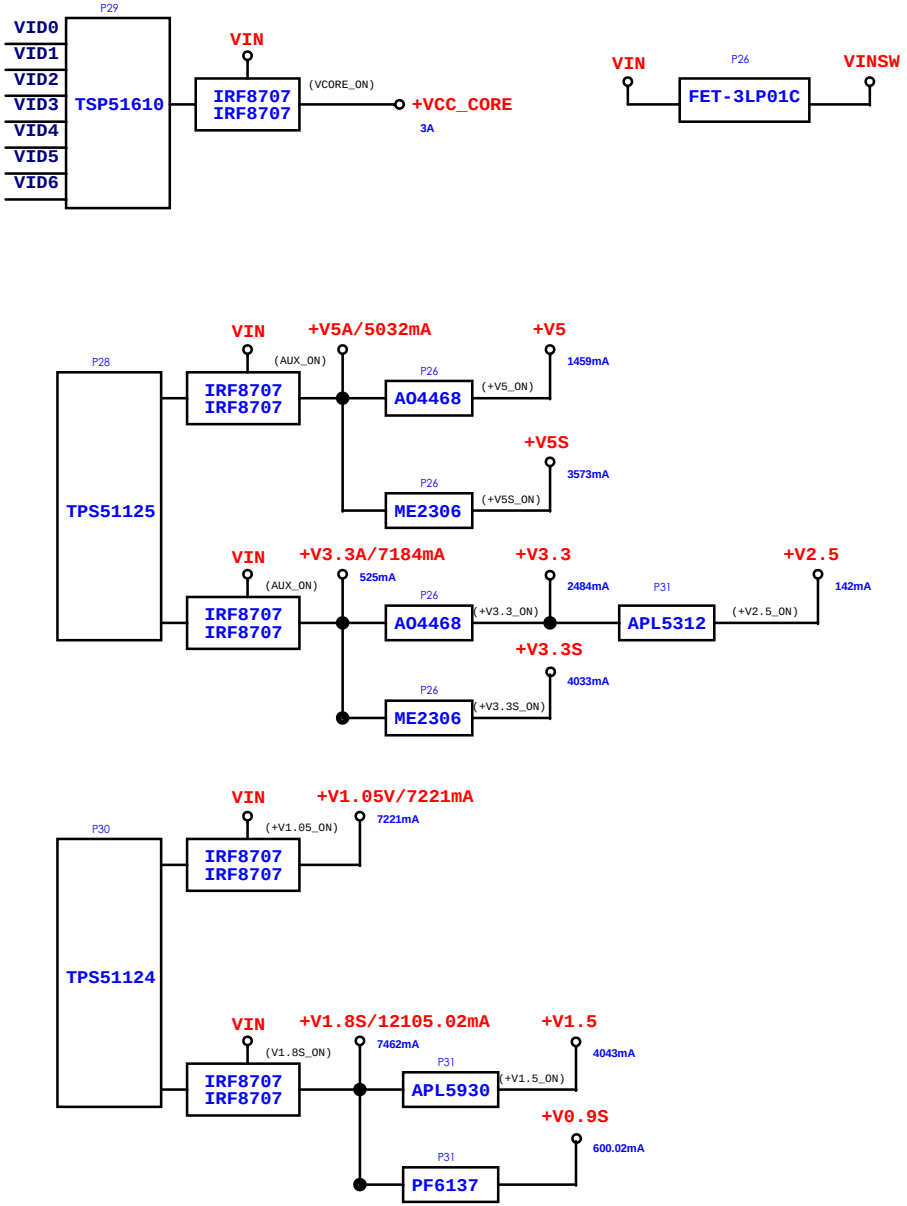
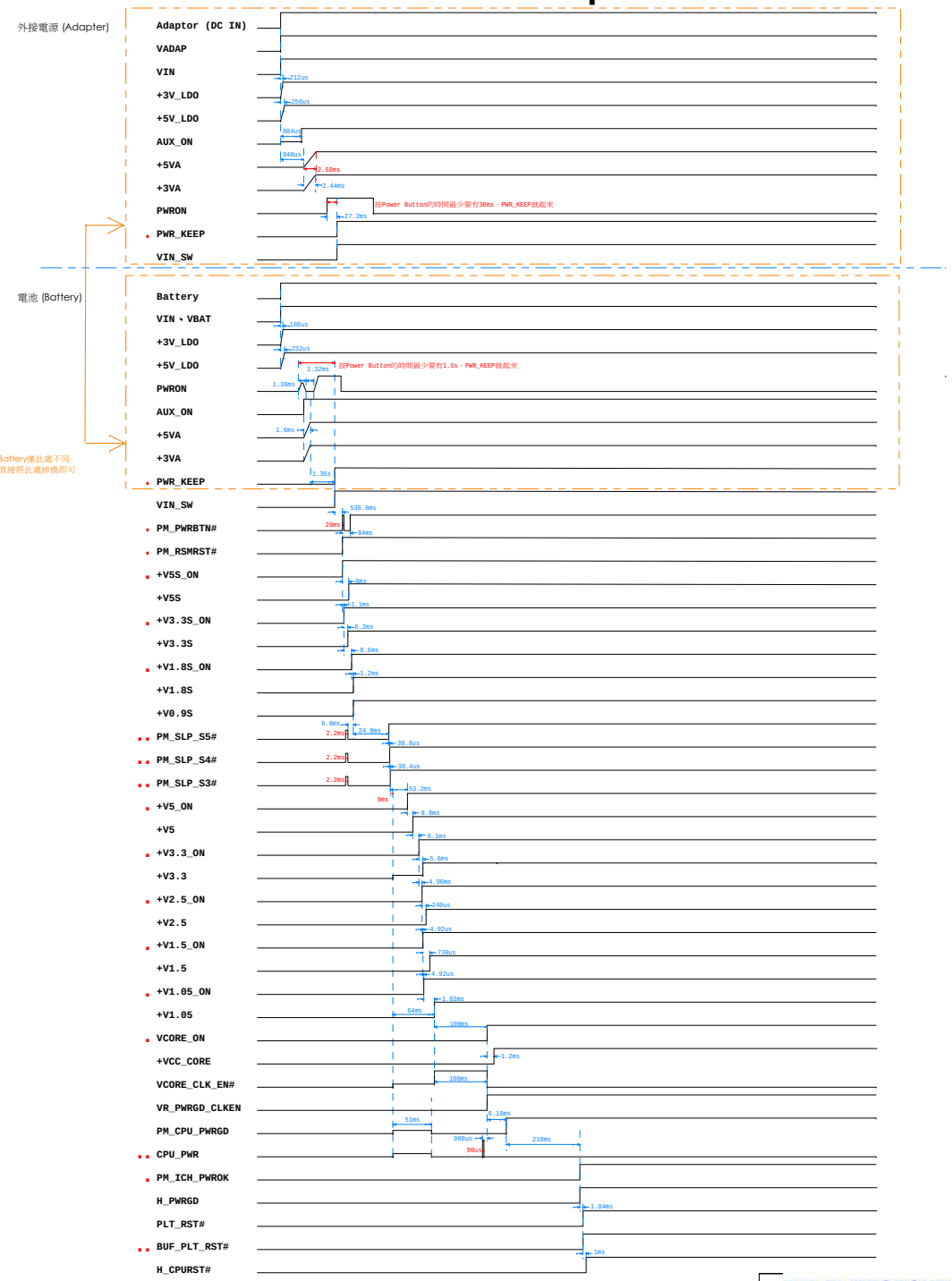
POWER BLOCK DIAGRAM



V10ILX M/B Power Rail State :

	+*V_LDO	+*VA	+*VS	+*V	CLK
AC/DC S0/Moff (Full On)	ON	ON	ON	ON	ON
AC/DC S3/Moff (STR)	ON	ON	ON	OFF	Only MCH BCLK
AC/DC S4/Moff (STD)	ON	ON	OFF	OFF	OFF
AC S5/Moff (Soft Off)	ON	ON	OFF	OFF	OFF
DC S5/Moff (Soft Off)	ON	OFF	OFF	OFF	OFF

POWER Sequence



. EC Control Pin

.. to EC Control Pin

ECS ELITEGROUP

Project : V10IL1

Size	Document Number	Rev
	POWER BLOCK DIAGRAM & Sequence	B

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ITE8502E GPIO

	GPA0	BTL_BEEP
	GPA1	EC_BL_PWM
	GPA2	NC
3/23名稱修正，3G_EC#改爲3G_EC	GPA3	3G_EC
	GPA4	CHG_R_LED#
	GPA5	CHG_G_LED#
	GPA6	NC
	GPA7	NC
	GPB0	NC
	GPB1	SMB_CLK_GEN
	GPB2	SMB_DATA_GEN
	GPB3	SMB_CLK_BAT#R
	GPB4	SMB_DATA_BAT
	GPB5	H_A20GATE
	GPB6	H_RCIN#
	GPB7	PWR_KEEP
	GPC0	NC
	GPC1	NC
	GPC2	NC
02/13 更換GPIO	GPC3	PWR_LED
GPC4 01/19 因應G-SENEOR所新增	GPC4	NC
02/13 更換GPIO 04/09 B Phase已無使用，刪除	GPC5	+V2.5_ON
	GPC6	NC
	GPC7	BT_EN#
1/19 +V5S_ON移除不用 1/20 pin16因應BLUETOOTH所新增	GPD0	AC_IN
	GPD1	WEBCAM_EN
	GPD2	PLT_RST#_EC
	GPD3	EC_SCI#
	GPD4	EC_EXTSMI#
	GPD5	NC
02/06 新增Low Voltage#	GPD6	Low Voltage#
	GPD7	PM_PWRBTN#
	GPE0	BKL_EC
	GPE1	MUTE_AMP#
	GPE2	PM_RSMRST#
	GPE3	PM_SYSRST#
	GPE4	PWR_SW
	GPE5	PM_SLP_S3#
	GPE6	PM_SLP_S4#
	GPE7	LID#
	GPF0	RF_LED
	GPF1	RF_OFF#
	GPF2	NC
	GPF3	NC
	GPF4	PS2_CLK_TP
	GPF5	PS2_DATA_TP
	GPF6	NC
	GPF7	NC
1/17 更名爲3G_ONEC#	GPG0	3G_ONEC#
	GPG1	+V1.8S_ON
	GPG2	FLFRAME#
	GPG6	+V1.5_ON
	GPH0	PM_ICH_PWROK
	GPH1	VCORE_ON
	GPH2	+V5_ON
	GPH3	+V3.3_ON
	GPH4	+V3.3S_ON
1/17新增	GPH5	+V5S_ON
	GPH6	+V1.05_ON

ITE8502E GPIO

GPI0	BATT_TEMP
GPI1	ADAPTOR_I
GPI2	BAT_I
GPI3	BAT_V
GPI4	SAFTY_PROTECT
GPI5	EC_PROCHOT#
GPI6	CPU_PWR
GPI7	PM_SLP_S5#
GPJ0	Fast-charge-EN
GPJ1	CHG_I
GPJ2	FAN_CTRL0
GPJ3	SENBAT_V
GPJ4	CHG_V
GPJ5	CHG_ON

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Size	Document Number	Rev
	GPIO & POWER Consumption	B

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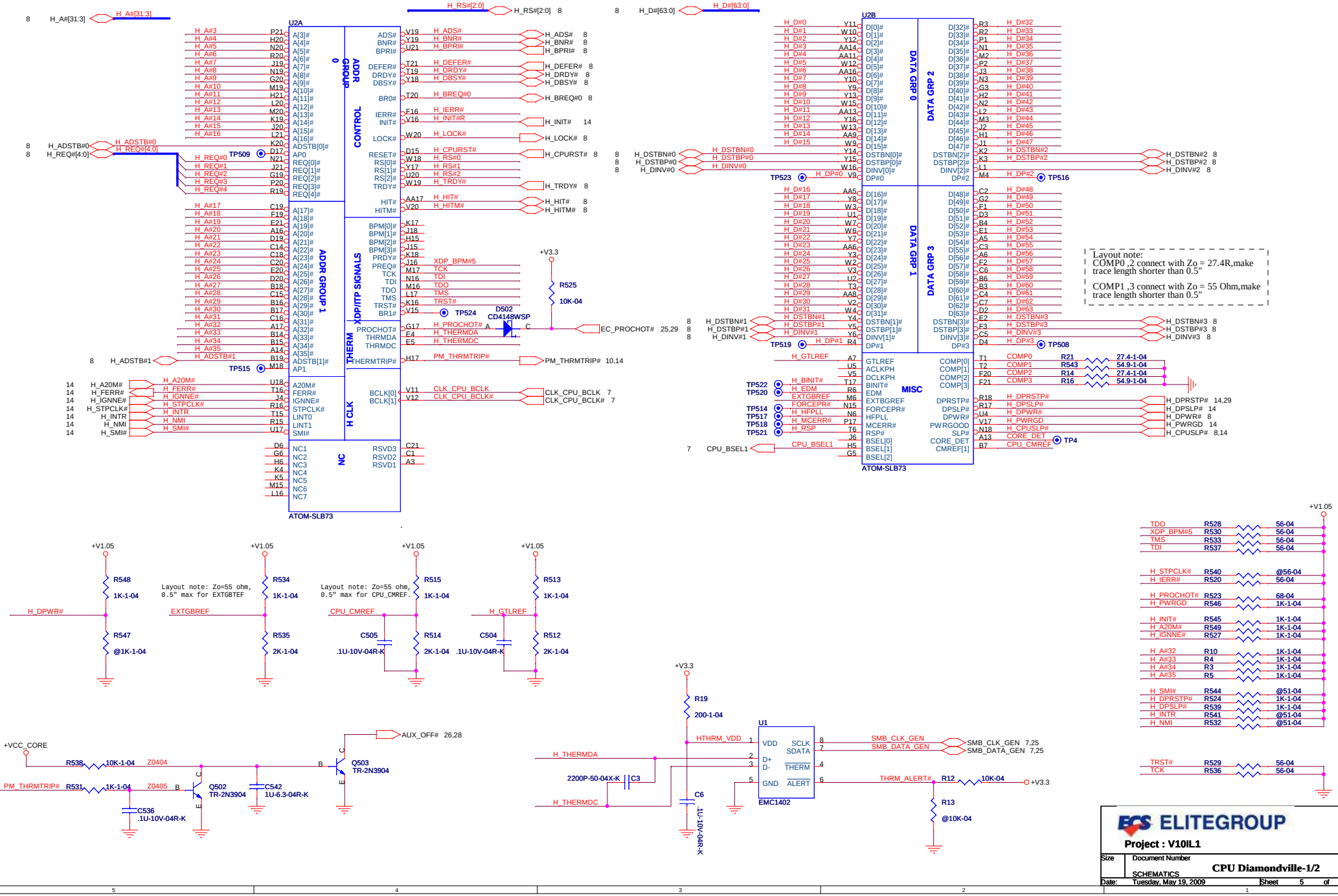
U2A
ADDR GROUP 0
ADDR GROUP 1
CONTROL
XDP/ITP SIGNALS
THERM
H CLK
NC
ATOM-SLB73
U2B
DATA GRP 0
DATA GRP 1
DATA GRP 2
DATA GRP 3
MISC
ATOM-SLB73
Layout note:
COMP0 ,2 connect with Zo = 27.4R,make trace length shorter than 0.5"
COMP1 ,3 connect with Zo = 55 Ohm,make trace length shorter than 0.5"
Layout note: Zo=55 ohm, 0.5" max for EXTGBTEF
Layout note: Zo=55 ohm, 0.5" max for CPU_CMREF.
U1
EMC1402
Q502 TR-2N3904
Q503 TR-2N3904
D502 CD4148WSP
TP509
TP515
TP523
TP516
TP519
TP508
TP524
TP522
TP520
TP514
TP517
TP518
TP521
TP4

ELITEGROUP
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Size
Document Number
CPU Diamondville-1/2
Rev B
SCHEMATICS
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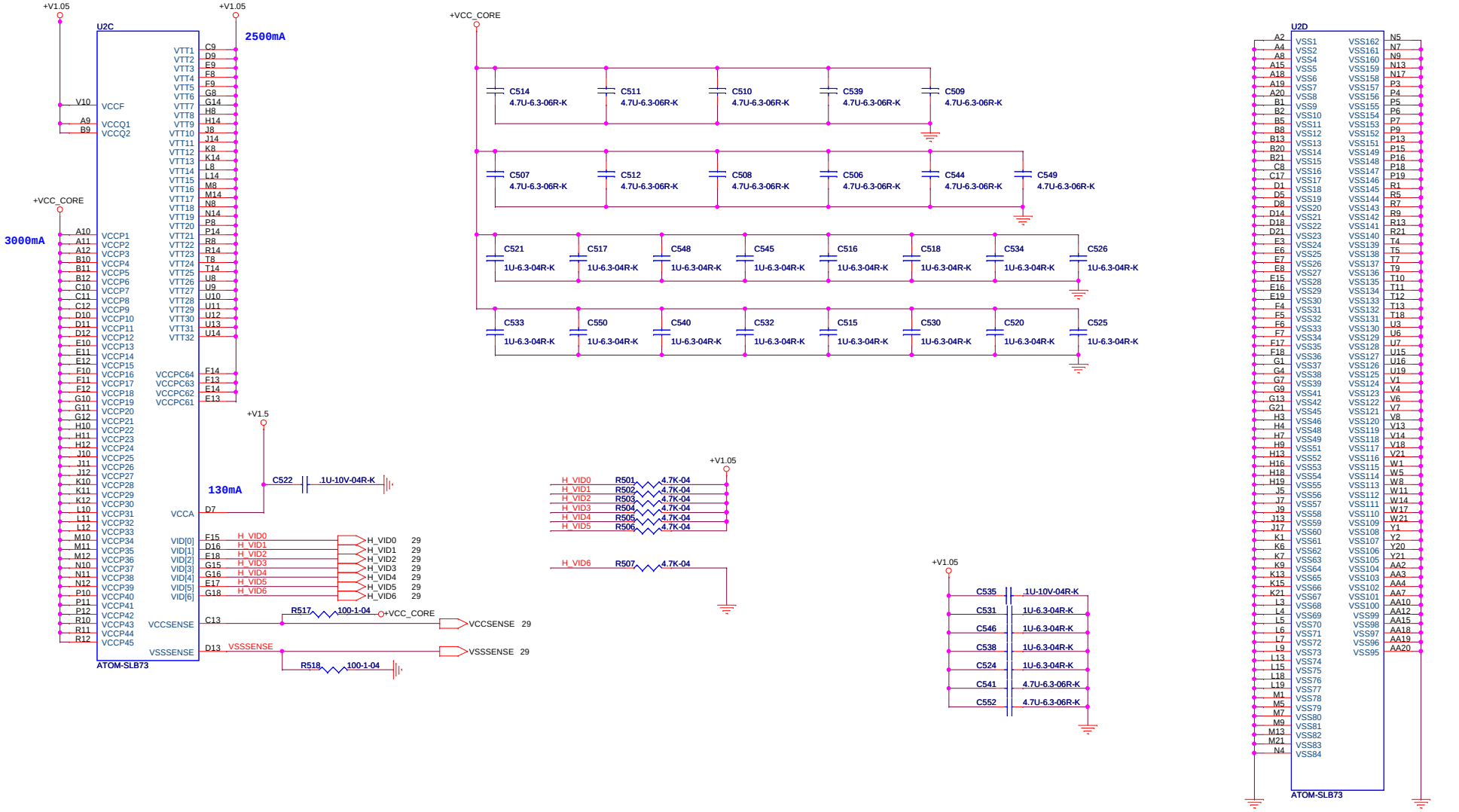

ELITEGROUP

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Size	Document Number	Rev
	CPU Diamondville-2/2	B
	SCHEMATICS	
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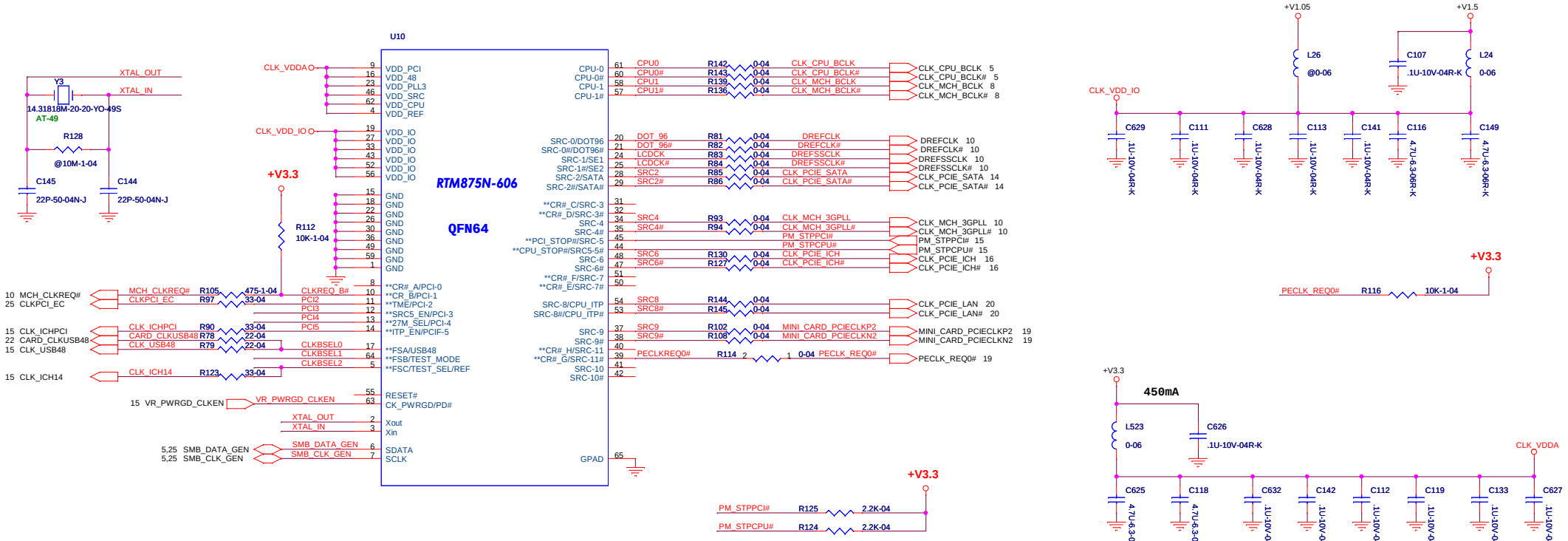

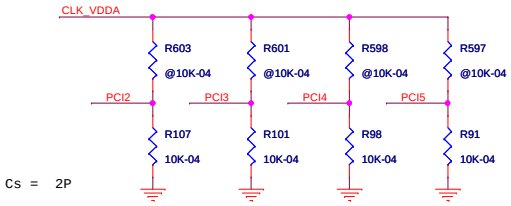

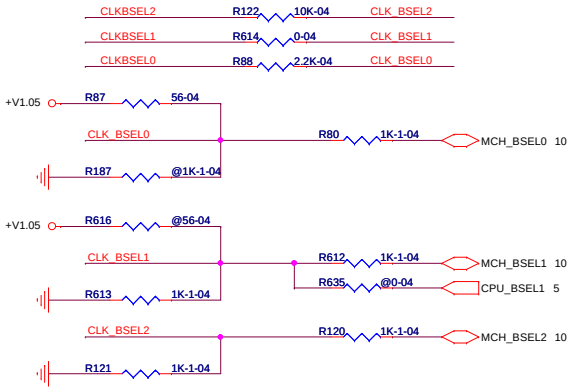

	PULL HIGH	PULL LOW
PCI-2	NO OVERCLOCKING	NORMAL RUN
PCI-3	Pin 45/44 is SRC5	Pin 45/44 is PCI_STOP/CPU_STOP
PCI-4	Pin 20/21 is SRC-0 Pin 24/25 is 27/27SS	Pin 20/21 is DOT96 Pin 24/25 is SRC-1
PCI-5	Pin 54/53 is CPUITP	Pin 54/53 is SRC8

BESL2 FSC	BESL1 FSB	BESL0 FSA	CPU
0	0	0	266
0	0	1	133
0	1	0	200
0	1	1	166
1	0	0	333
1	0	1	100
1	1	0	400
1	1	1	200

ECS ELITEGROUP

Project : V10IL1

Size	Document Number	Rev
	SCHEMATICS Clock Generator RTM875N-606	B
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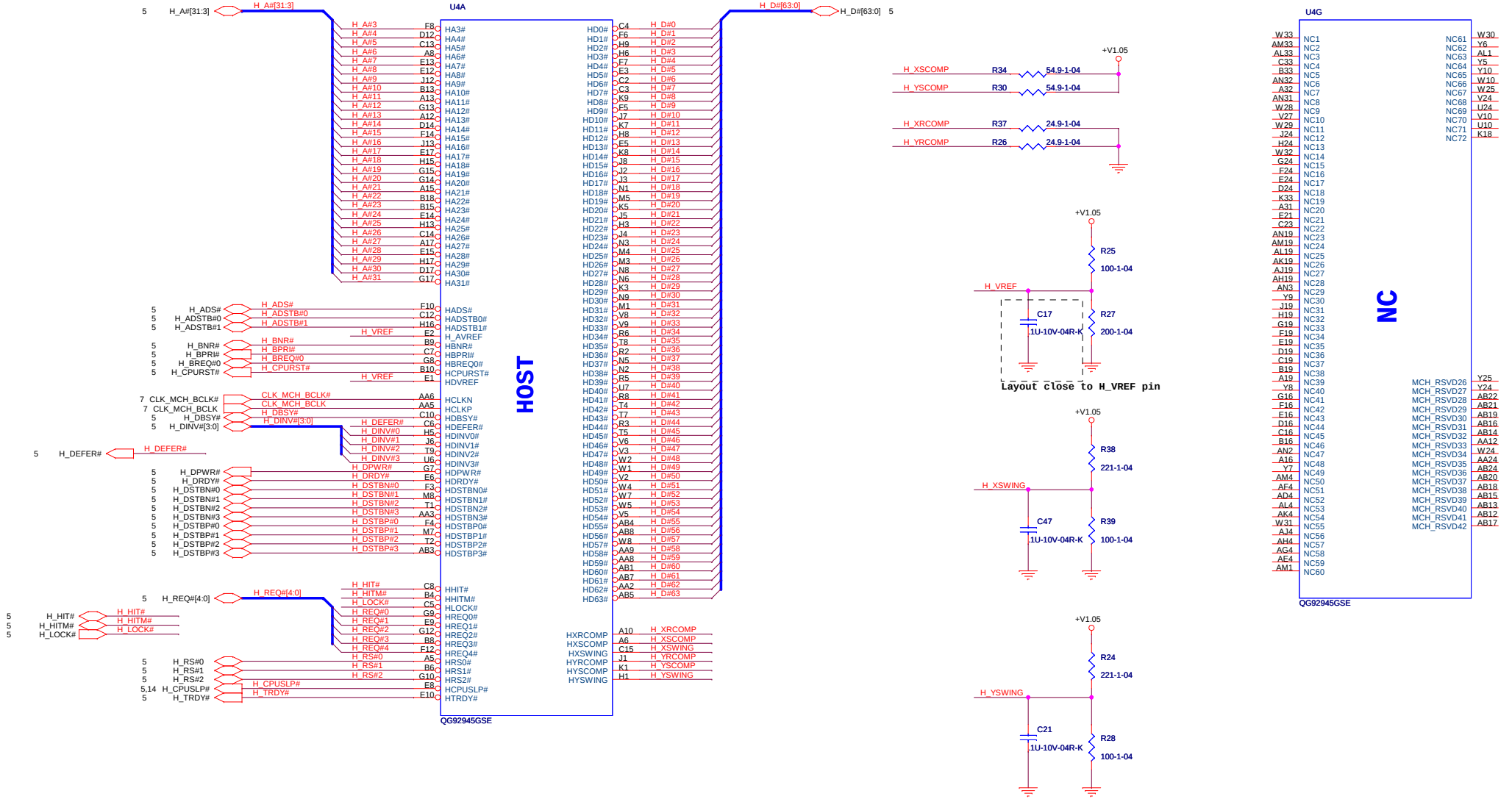

ELITEGROUP

Project : V10IL1

Size	Document Number	Rev
	SCHEMATICS 945GSE- Host(1/4)	B
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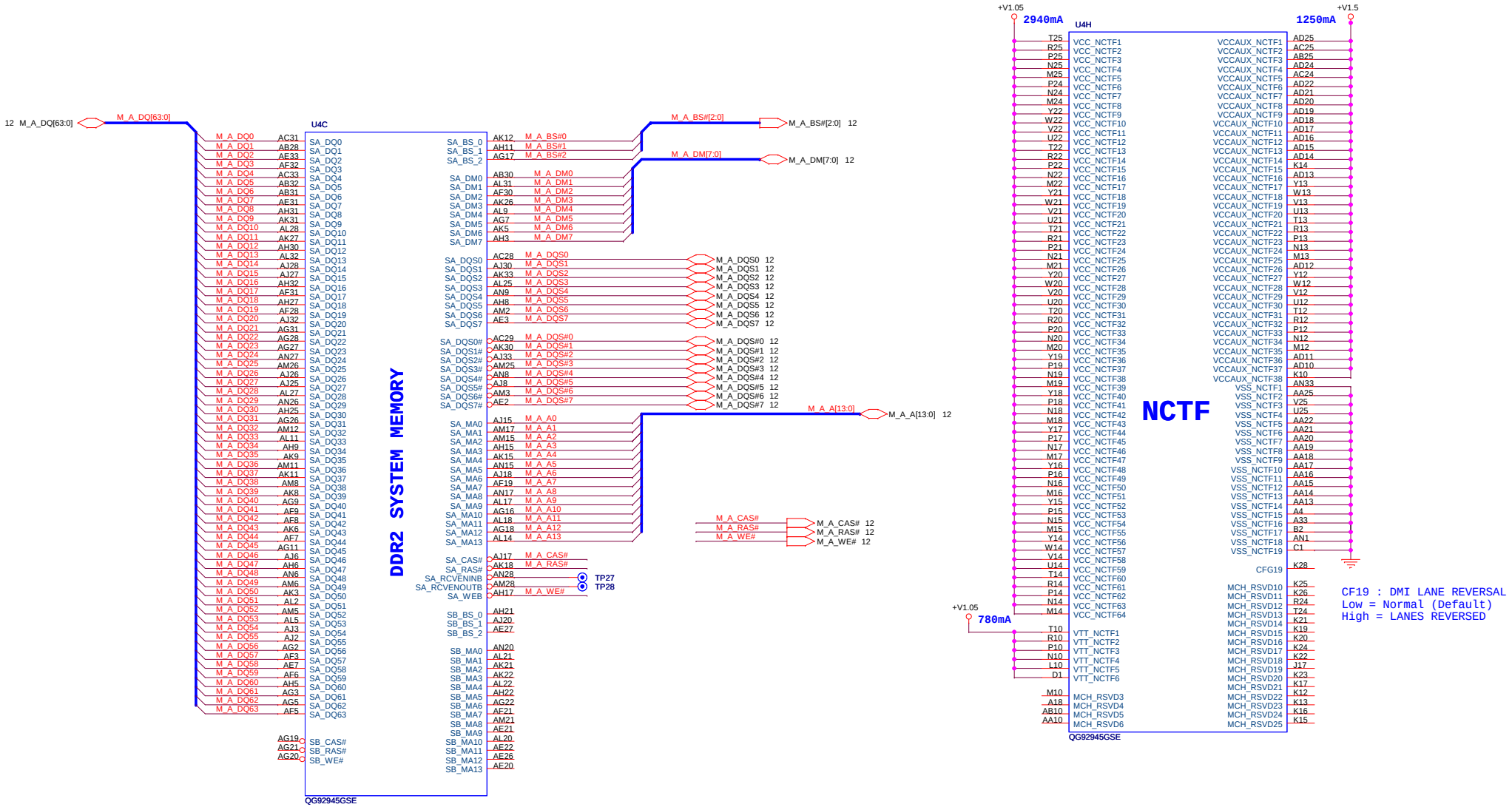

ELITEGROUP

Project : V10IL1

Size	Document Number	Rev
	945GSE- DDR(2/4)	B
	SCHEMATICS	
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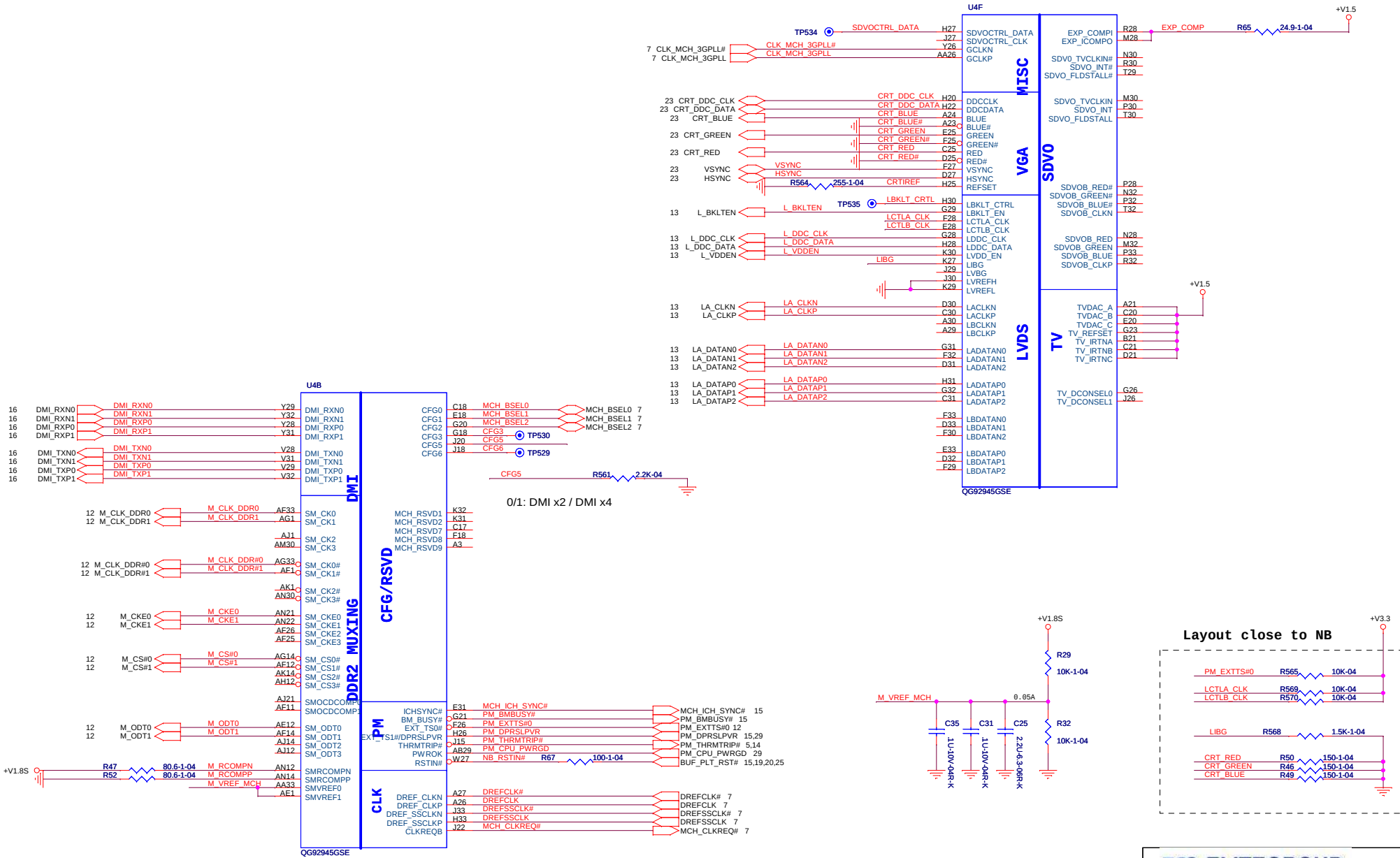
U4F
MISC
VGA
SDVO
LVDS
TV
QG92945GSE
U4B
DMI
DDR2 MUXING
CFG/RSVD
PM
CLK
QG92945GSE
0/1: DMI x2 / DMI x4
Layout close to NB
ELITEGROUP
Project : V10IL1
945GSE- DMI & VGA(3/4)
SCHEMATICS
Rev B
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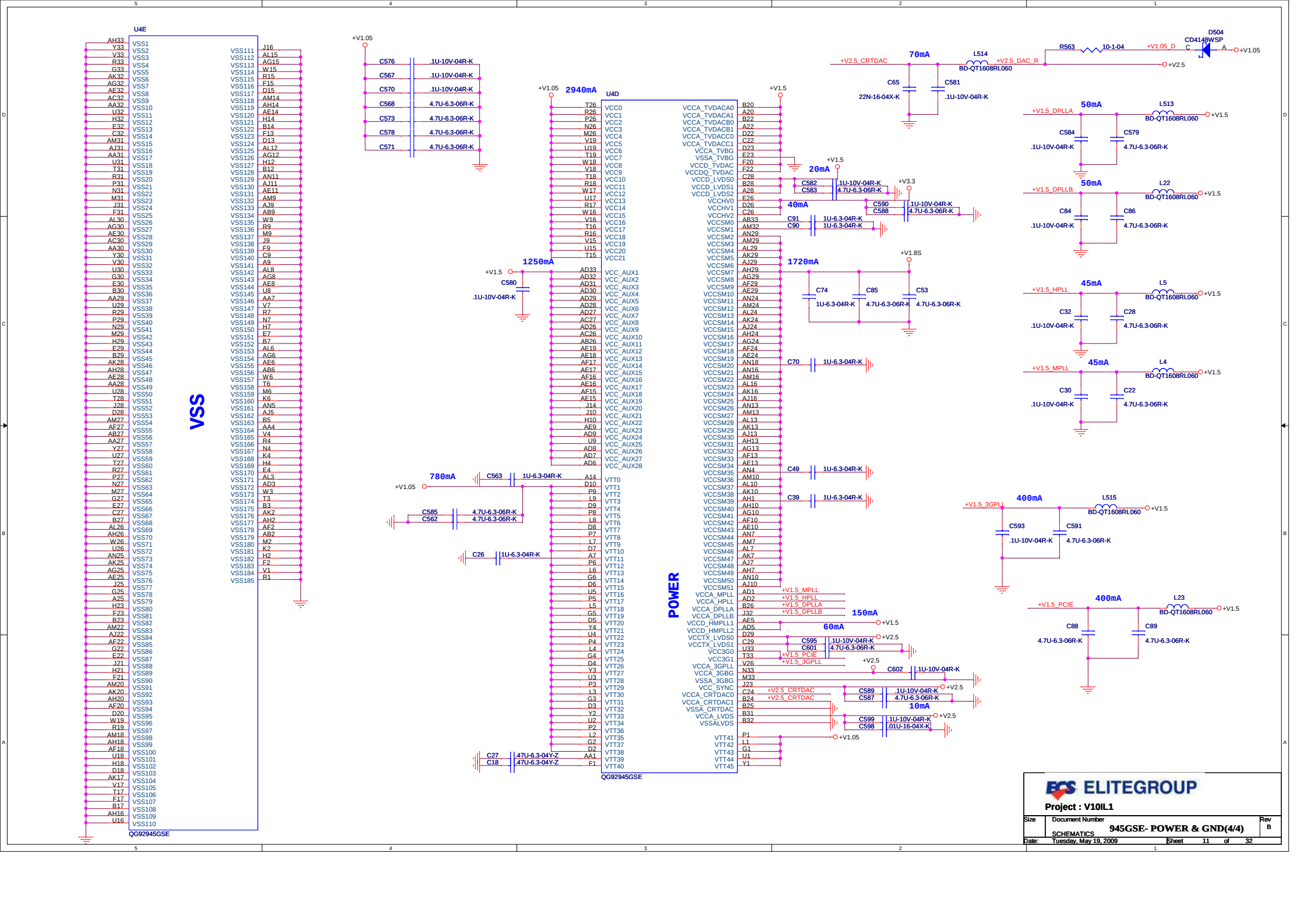
U4E
VSS
QG92945GSE
U4D
POWER
QG92945GSE
2940mA
1250mA
780mA
1720mA
70mA
20mA
40mA
60mA
150mA
10mA
50mA
45mA
400mA
L514
BD-QT1608RL060
L513
L22
L5
L4
L515
L23
D504
CD4148WSP
R563
10-1-04
ELITEGROUP
Project : V10IL1
Size
Document Number
945GSE- POWER & GND(4/4)
Rev
B
SCHEMATICS
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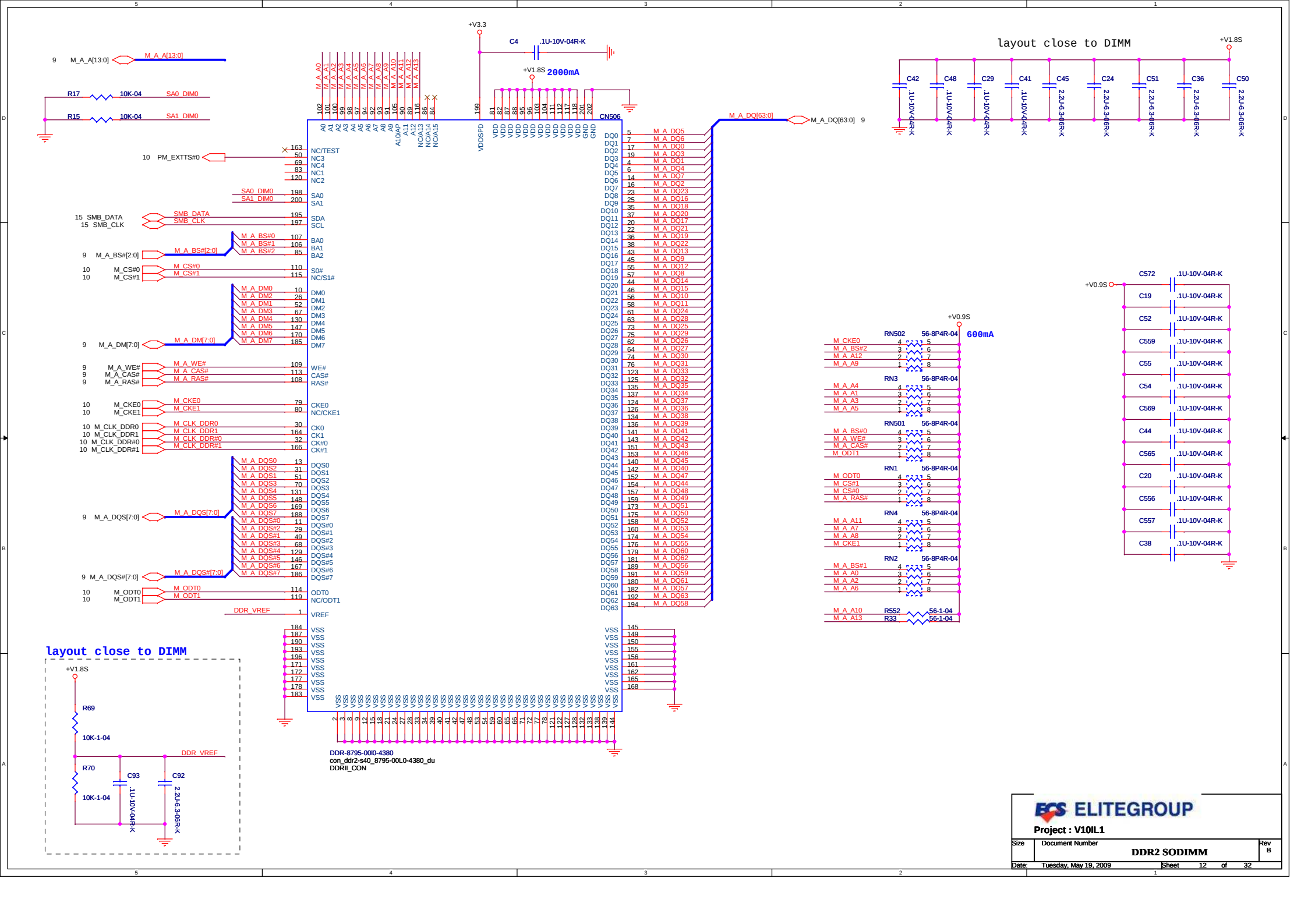
layout close to DIMM
+V1.8S
C42 .1U-10V-04R-K
C48 .1U-10V-04R-K
C29 .1U-10V-04R-K
C41 .1U-10V-04R-K
C45 2.2U-6.3-06R-K
C24 2.2U-6.3-06R-K
C51 2.2U-6.3-06R-K
C36 2.2U-6.3-06R-K
C50 2.2U-6.3-06R-K
+V3.3
C4 .1U-10V-04R-K
+V1.8S 2000mA
CN506
M_A_A[13:0]
M_A_DQ[63:0]
R17 10K-04 SA0_DIM0
R15 10K-04 SA1_DIM0
PM_EXTTS#0
SMB_DATA
SMB_CLK
M_A_BS#[2:0]
M_CS#0
M_CS#1
M_A_DM[7:0]
M_A_WE#
M_A_CAS#
M_A_RAS#
M_CKE0
M_CKE1
M_CLK_DDR0
M_CLK_DDR1
M_CLK_DDR#0
M_CLK_DDR#1
M_A_DQS[7:0]
M_A_DQS#[7:0]
M_ODT0
M_ODT1
DDR_VREF
NC/TEST
SA0
SA1
SDA
SCL
BA0
BA1
BA2
S0#
NC/S1#
DM0
DM1
DM2
DM3
DM4
DM5
DM6
DM7
WE#
CAS#
RAS#
CKE0
NC/CKE1
CK0
CK1
CK#0
CK#1
DQS0
DQS1
DQS2
DQS3
DQS4
DQS5
DQS6
DQS7
DQS#0
DQS#1
DQS#2
DQS#3
DQS#4
DQS#5
DQS#6
DQS#7
ODT0
NC/ODT1
VREF
VDDSPD
VDD
VSS
DDR-8795-00I0-4380
con_ddr2-s40_8795-00L0-4380_du
DDRII_CON
+V0.9S
600mA
RN502 56-8P4R-04
RN3 56-8P4R-04
RN501 56-8P4R-04
RN1 56-8P4R-04
RN4 56-8P4R-04
RN2 56-8P4R-04
M_A_A10 R552 56-1-04
M_A_A13 R33 56-1-04
C572 .1U-10V-04R-K
C19 .1U-10V-04R-K
C52 .1U-10V-04R-K
C559 .1U-10V-04R-K
C55 .1U-10V-04R-K
C54 .1U-10V-04R-K
C569 .1U-10V-04R-K
C44 .1U-10V-04R-K
C565 .1U-10V-04R-K
C20 .1U-10V-04R-K
C556 .1U-10V-04R-K
C557 .1U-10V-04R-K
C38 .1U-10V-04R-K
layout close to DIMM
+V1.8S
R69 10K-1-04
R70 10K-1-04
DDR_VREF
C93 .1U-10V-04R-K
C92 2.2U-6.3-06R-K
ELITEGROUP
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Size
Document Number
DDR2 SODIMM
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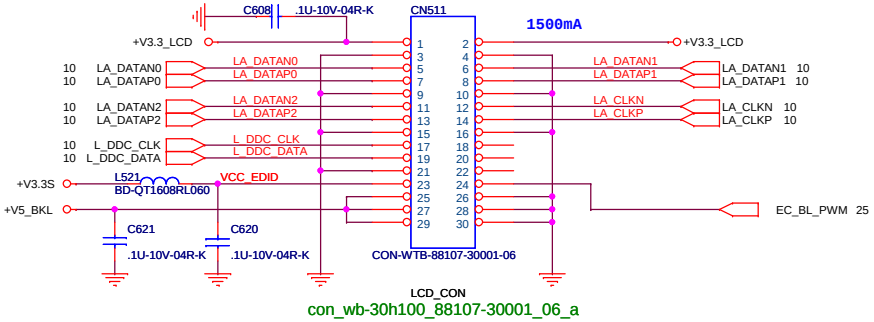

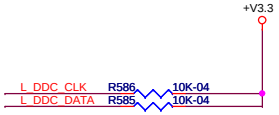

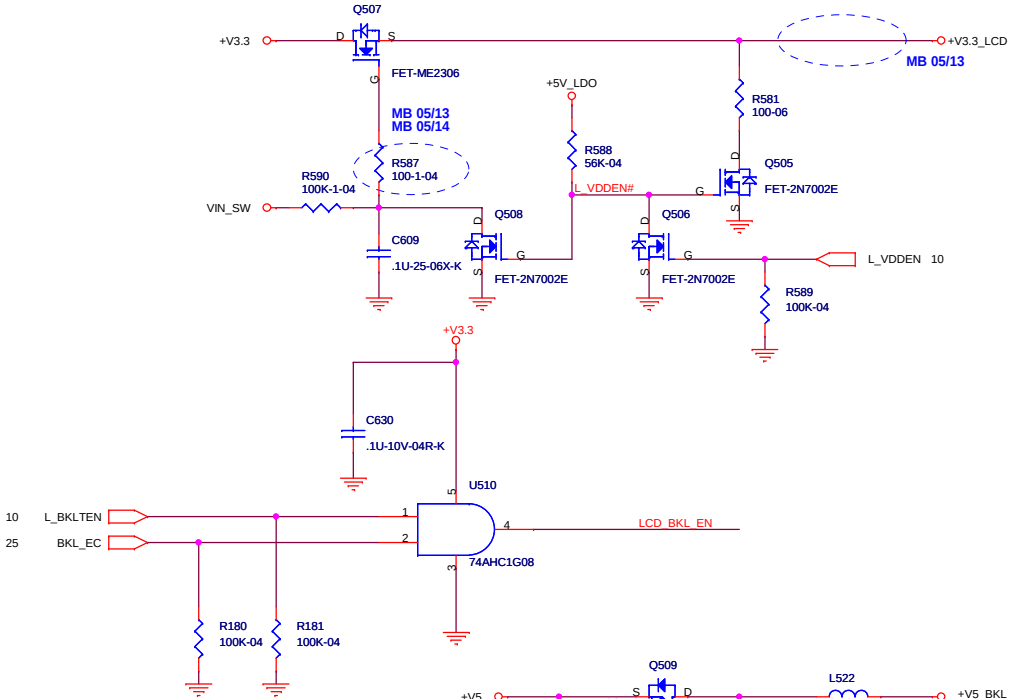

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Size	Document Number	Rev
	SCHEMATICS LCD	B
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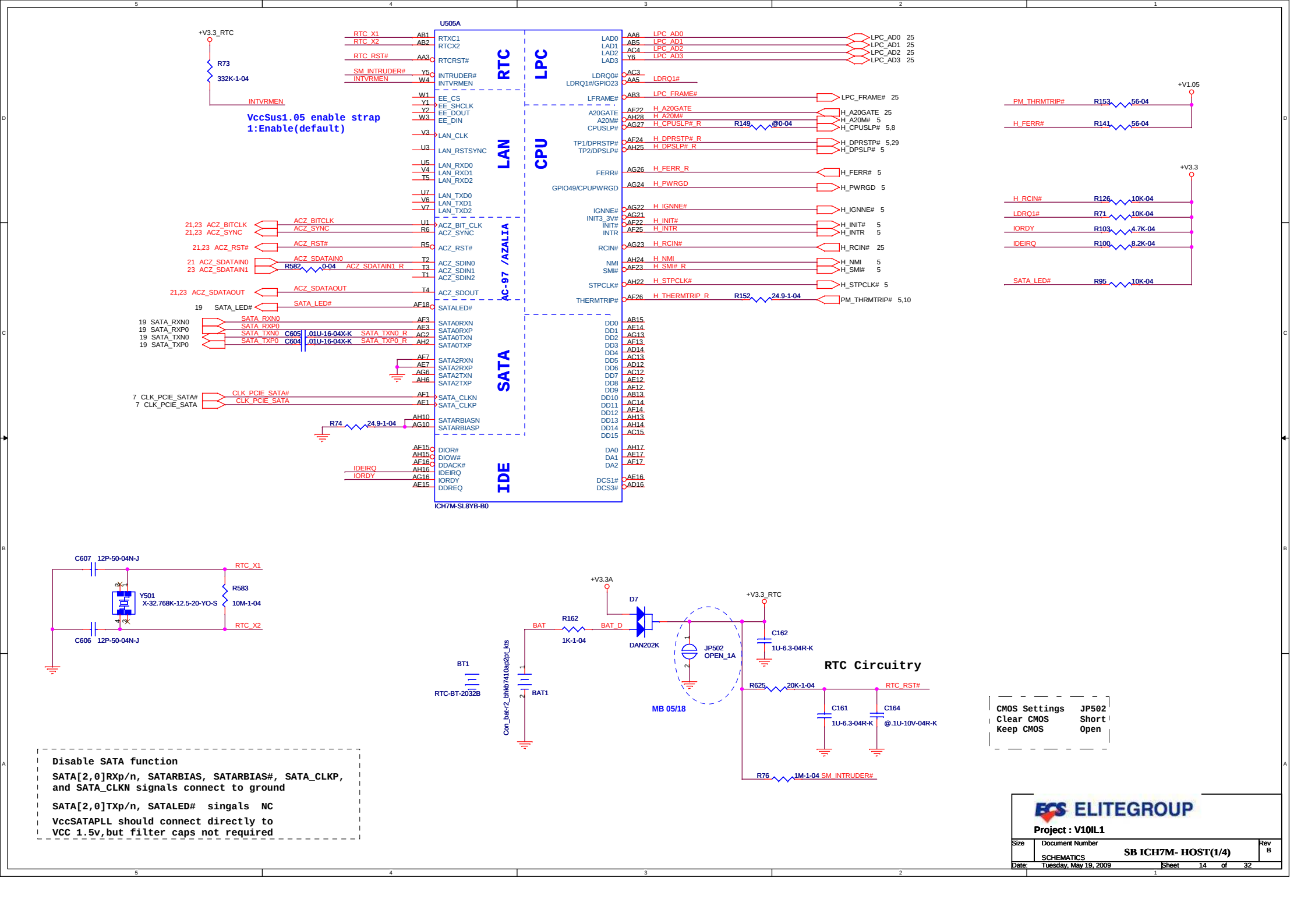
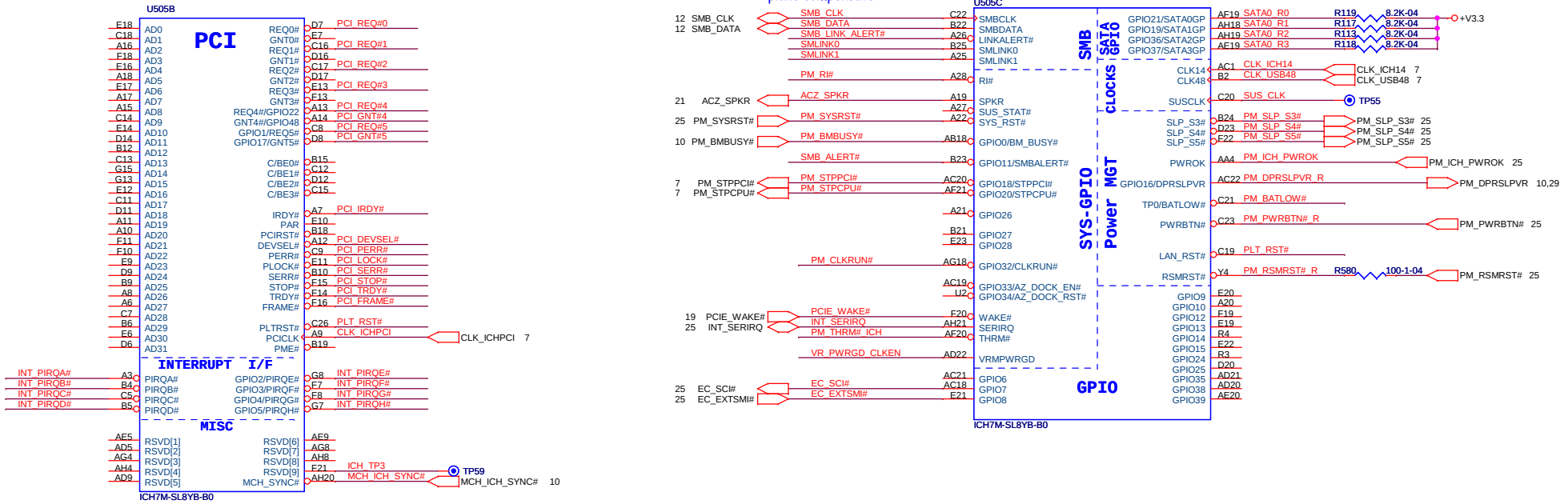
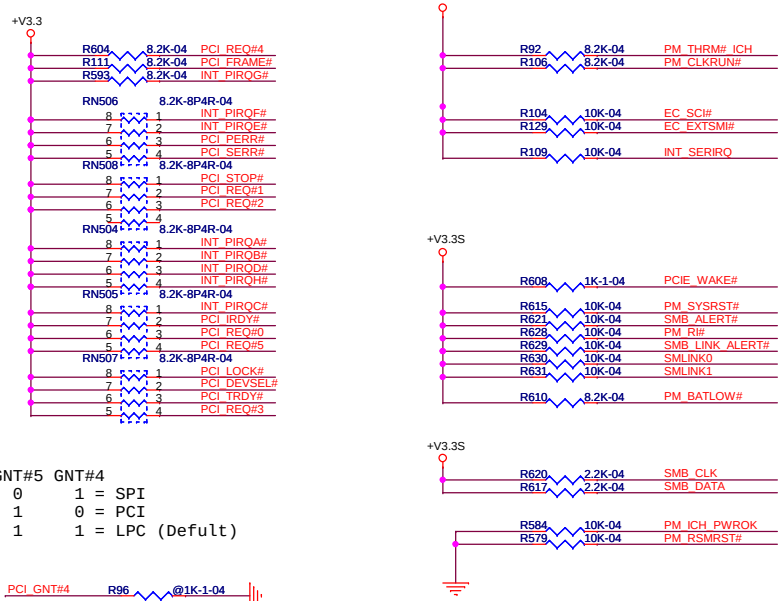
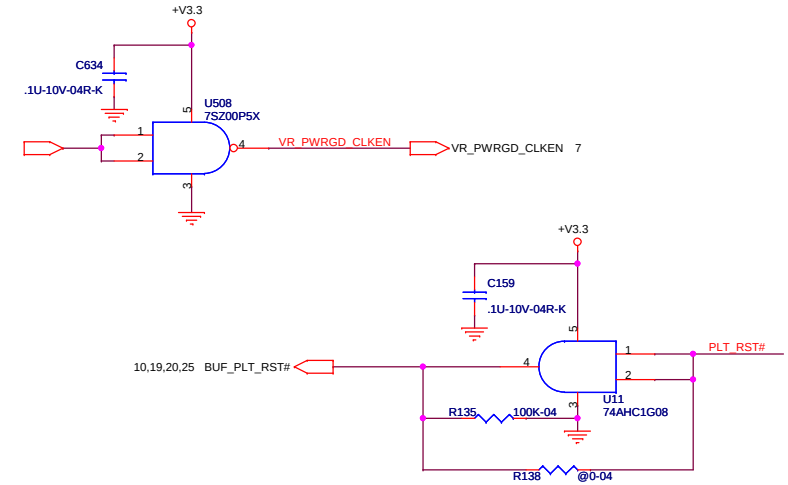

U505A
ICH7M-SL8YB-B0
RTC
LPC
LAN
CPU
AC-97 /AZALIA
SATA
IDE
VccSus1.05 enable strap
1:Enable(default)
Disable SATA function
SATA[2,0]RXp/n, SATARBIAS, SATARBIAS#, SATA_CLKP,
and SATA_CLKN signals connect to ground
SATA[2,0]TXp/n, SATALED# singals NC
VccSATAPLL should connect directly to
VCC 1.5v,but filter caps not required
RTC Circuitry
MB 05/18
CMOS Settings JP502
Clear CMOS Short
Keep CMOS Open
Project : V10IL1
SB ICH7M- HOST(1/4)
SCHEMATICS
Tuesday, May 19, 2009

Layout Note: Place stuffing option minimize stubs

for Singal cross power plane compensate







GNT#5 GNT#4
0 1 = SPI
1 0 = PCI
1 1 = LPC (Defult)

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Project : V10IL1

Size	Document Number	Rev
	SCHEMATICS SB ICH7M- PCI & GPIO(2/4)	B
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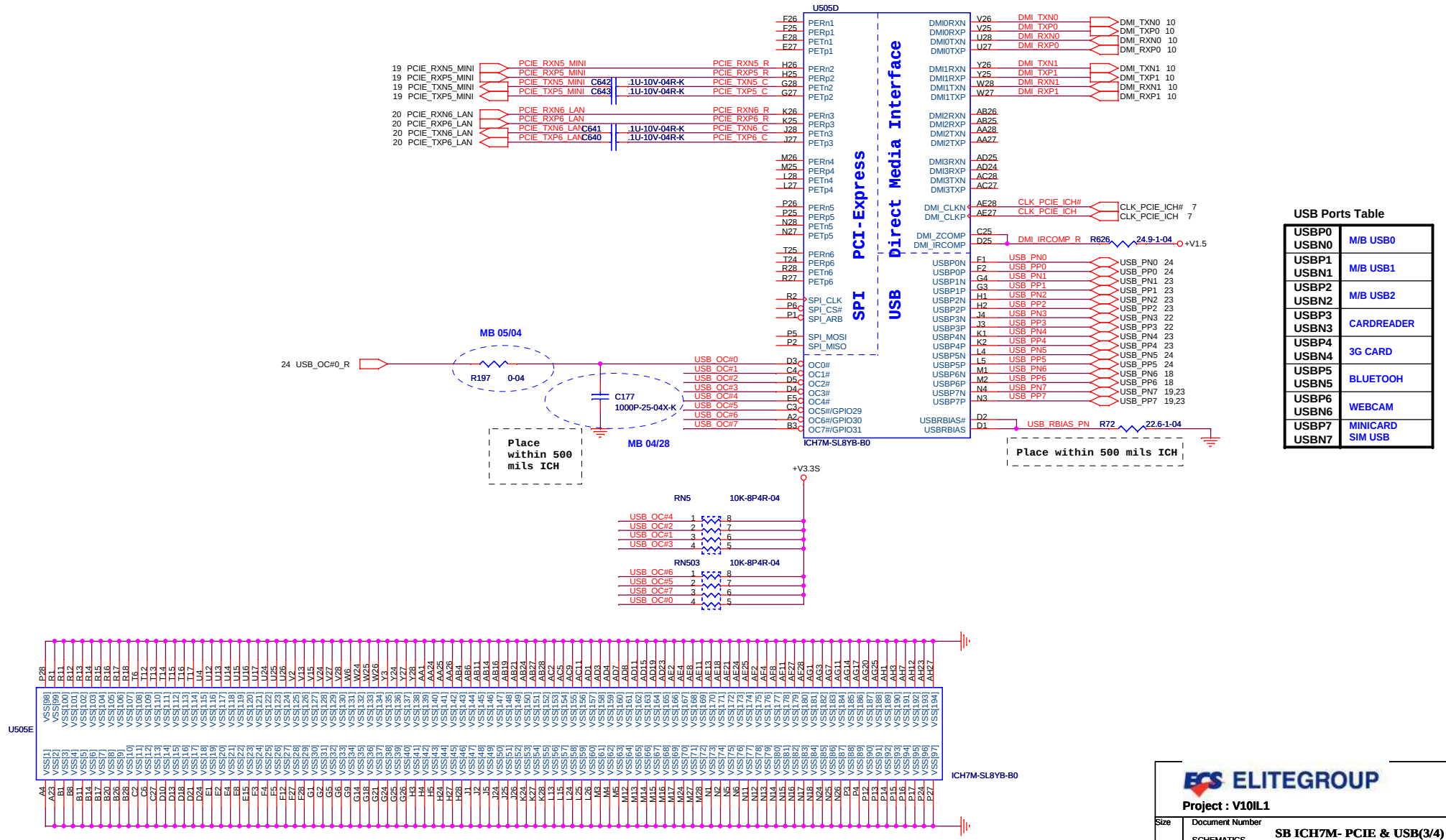


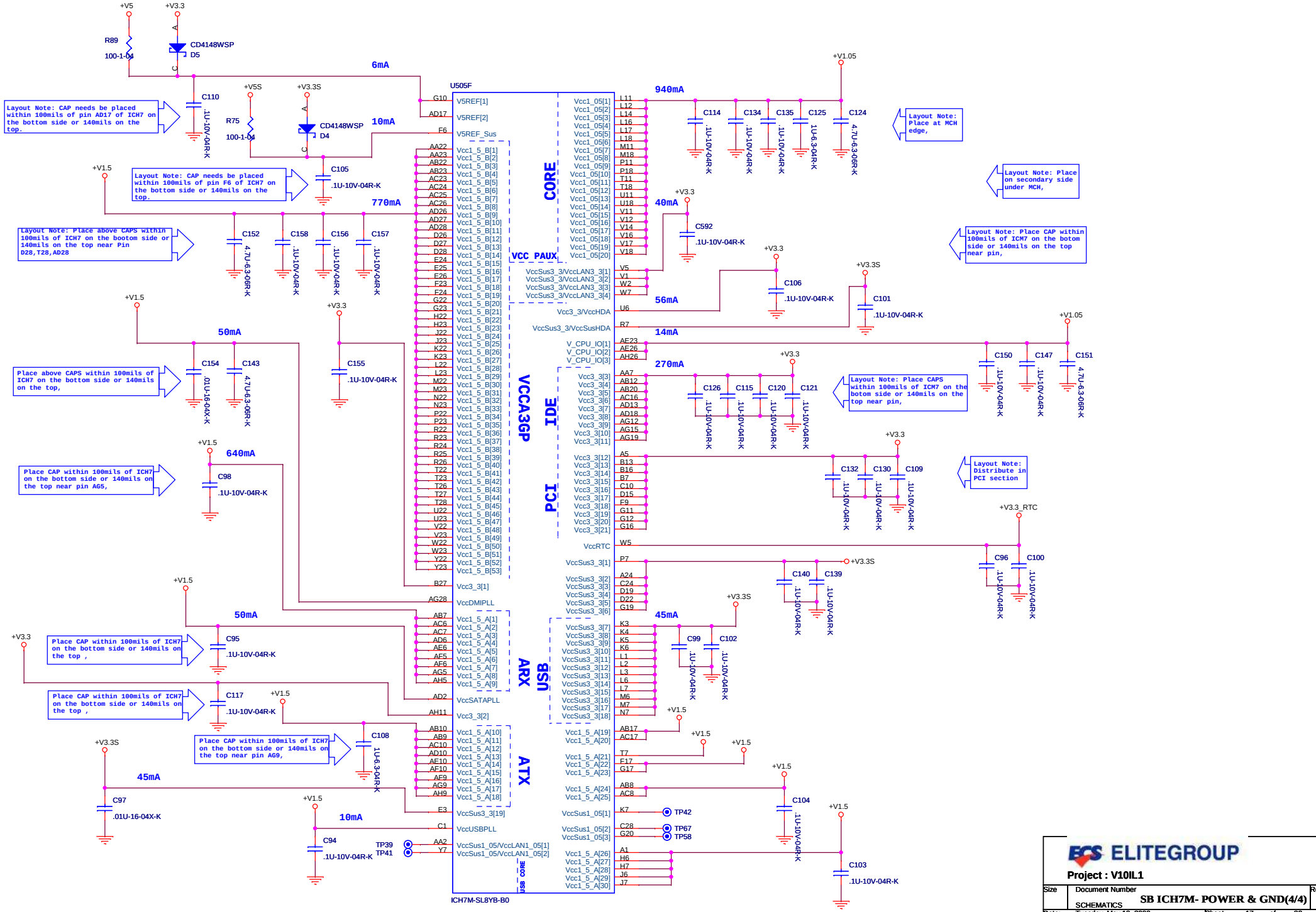
USB Ports Table

USBP0 USBN0	M/B USB0
USBP1 USBN1	M/B USB1
USBP2 USBN2	M/B USB2
USBP3 USBN3	CARDREADER
USBP4 USBN4	3G CARD
USBP5 USBN5	BLUETOOH
USBP6 USBN6	WEBCAM
USBP7 USBN7	MINICARD SIM USB

ELITEGROUP

Project : V10IL1

Size	Document Number	Rev
	SCHEMATICS SB ICH7M- PCIE & USB(3/4)	B
Date: Tuesday, May 19, 2009	Sheet 16 of 32	



ELITEGROUP

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Size	Document Number	Rev
	SCHEMATICS SB ICH7M- POWER & GND(4/4)	B
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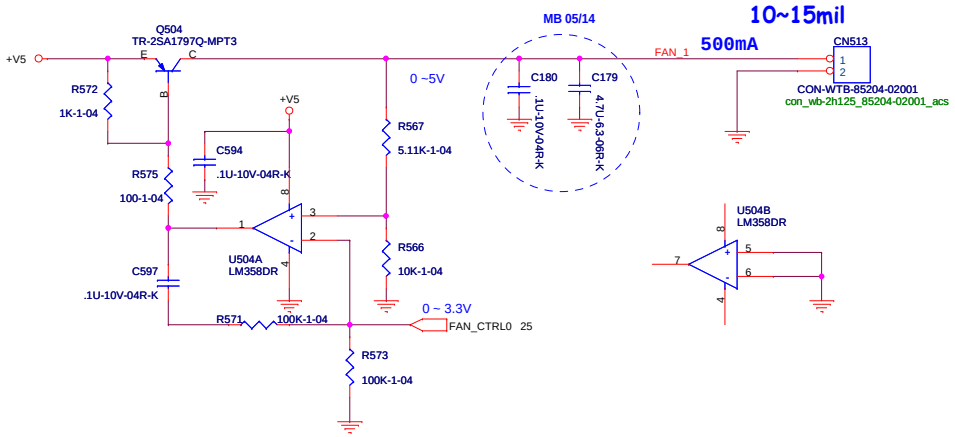
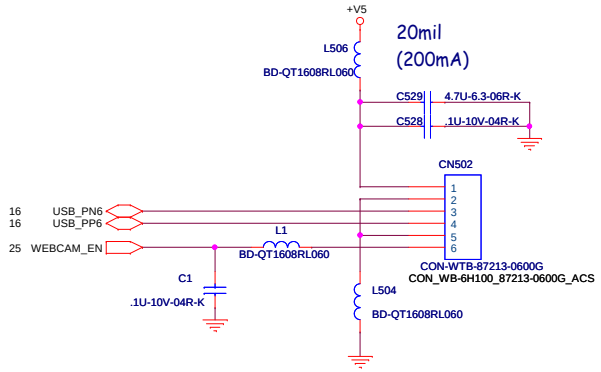
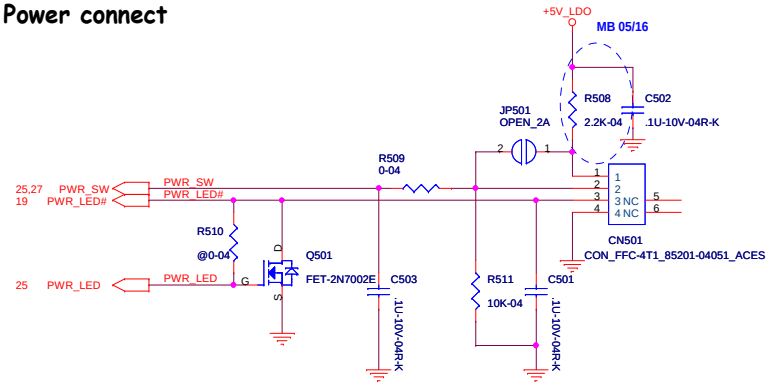
FAN control



Web CAM CON



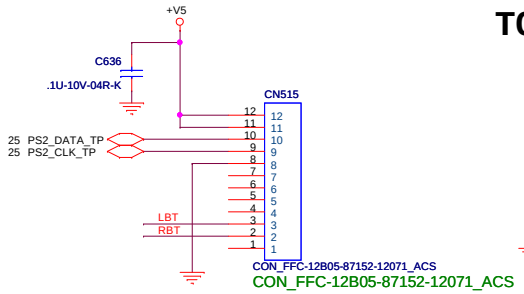
Power connect



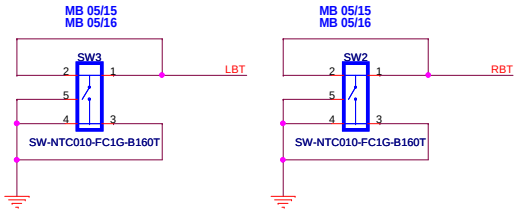
Touch PAD CON

MB 04/01

還原爲原先的Footprint以符合DXF



TOUCH PAD BUTTON

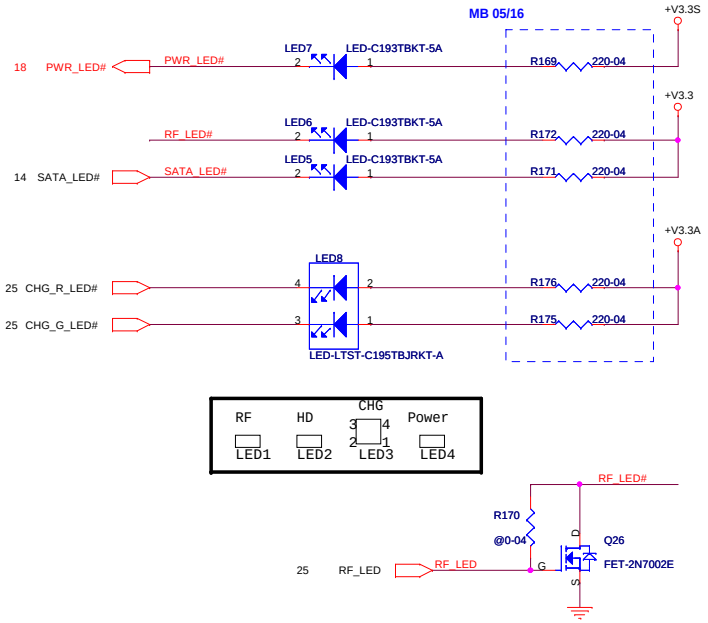


ELITEGROUP

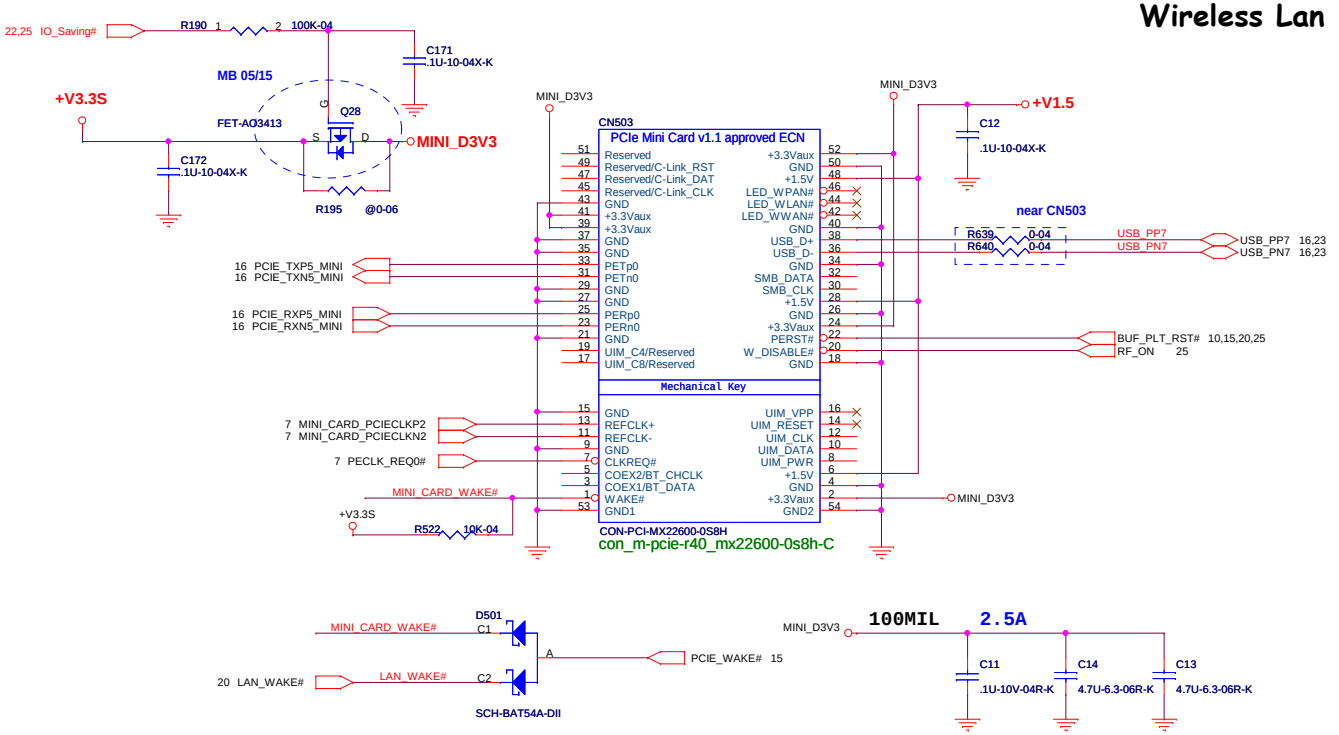
Project : V10IL1

Size	Document Number	Rev
	WEB CAM/TOUCH PAD/FAN/PW	B
	SCHEMATICS	
Date: Tuesday, May 19, 2009	Sheet 18 of 32	

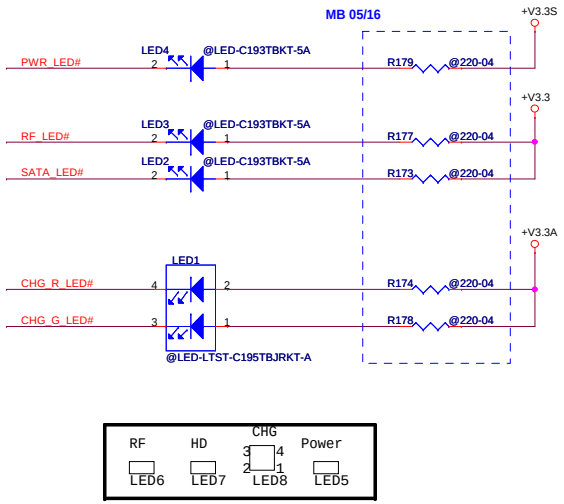
LED Display ID1



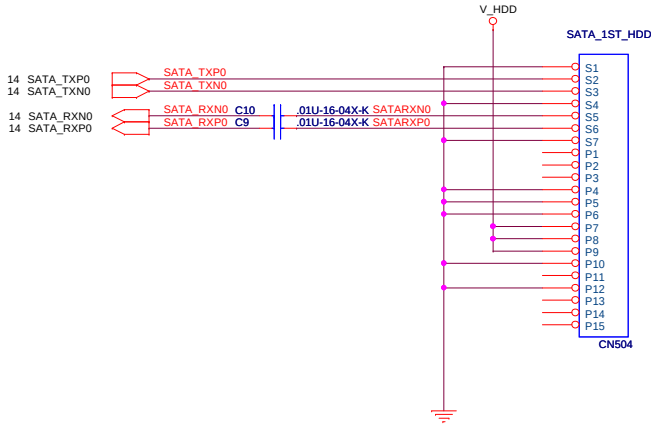
Wireless Lan



LED Display ID2

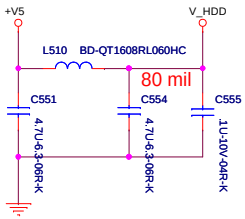


SATA Connector



LAYOUT: HDD Power

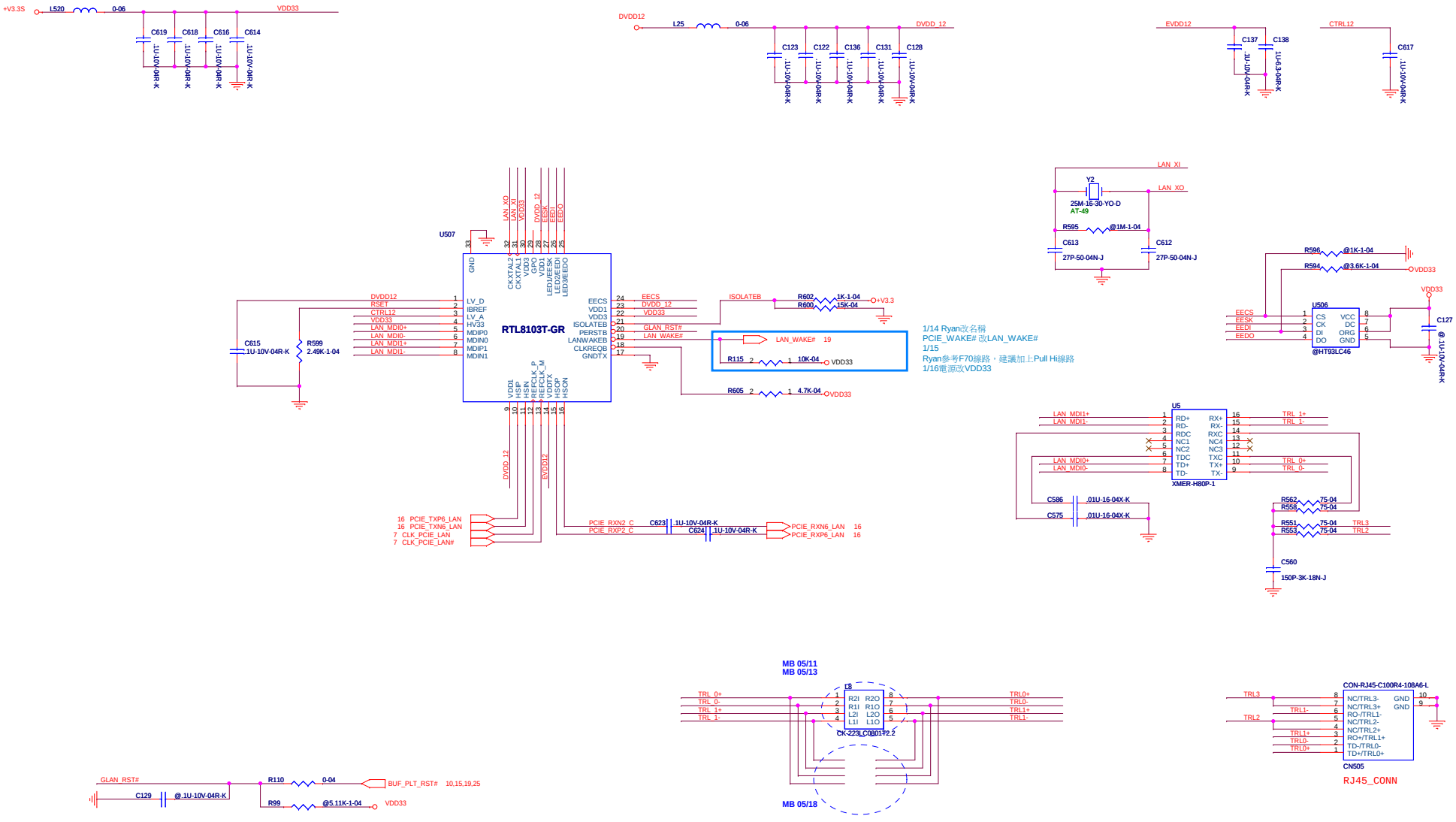
+5V_HDD: 50 mil / 700mA



ELITEGROUP

Project : V10IL1

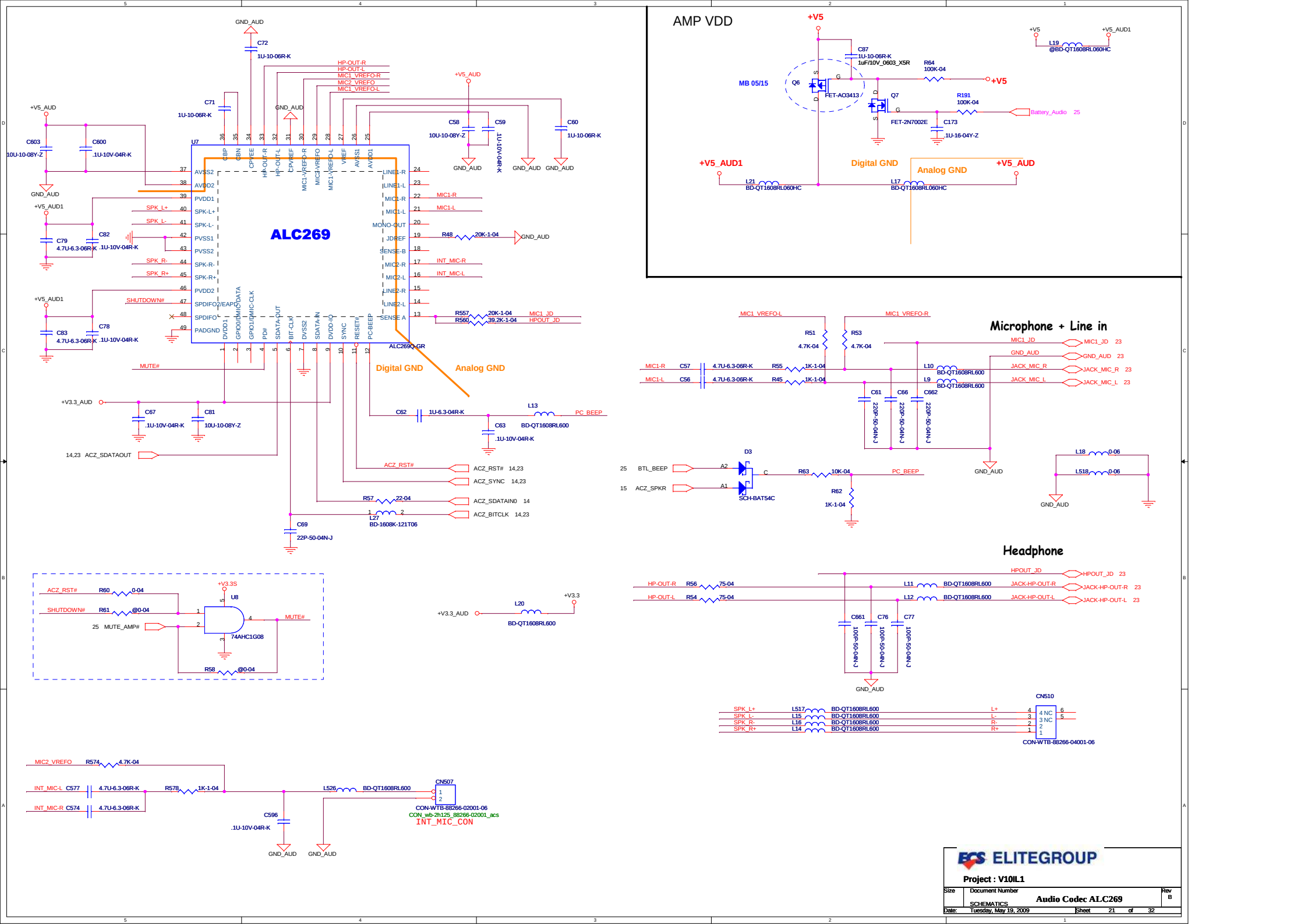
Size	Document Number	Rev
	SCHEMATICS LED/SATA/Wireless Lan	B
Date: Tuesday, May 19, 2009	Sheet 19 of 32	



ELITEGROUP

Project : V10IL1

Size	Document Number	Rev
	10/100M LAN- RTL8103T	B
	SCHEMATICS	
Date:	Tuesday, May 19, 2009	Sheet 20 of 32


ALC269
ALC269Q-GR
U7
AMP VDD
Microphone + Line in
Headphone
Digital GND
Analog GND
MB 05/15
Battery_Audio 25
ACZ_SDATAOUT
ACZ_RST#
ACZ_SYNC 14,23
ACZ_SDATAIN0 14
ACZ_BITCLK 14,23
INT_MIC_CON
CON-WTB-88266-04001-06
CON-WTB-88266-02001-06
CON_wb-2h125_88266-02001_acs
SCH-BAT54C
74AHC1G08
BD-QT1608RL600
BD-QT1608RL060HC
BD-1608K-121T06
ELITEGROUP
Project : V10IL1
Audio Codec ALC269
SCHEMATICS
Tuesday, May 19, 2009
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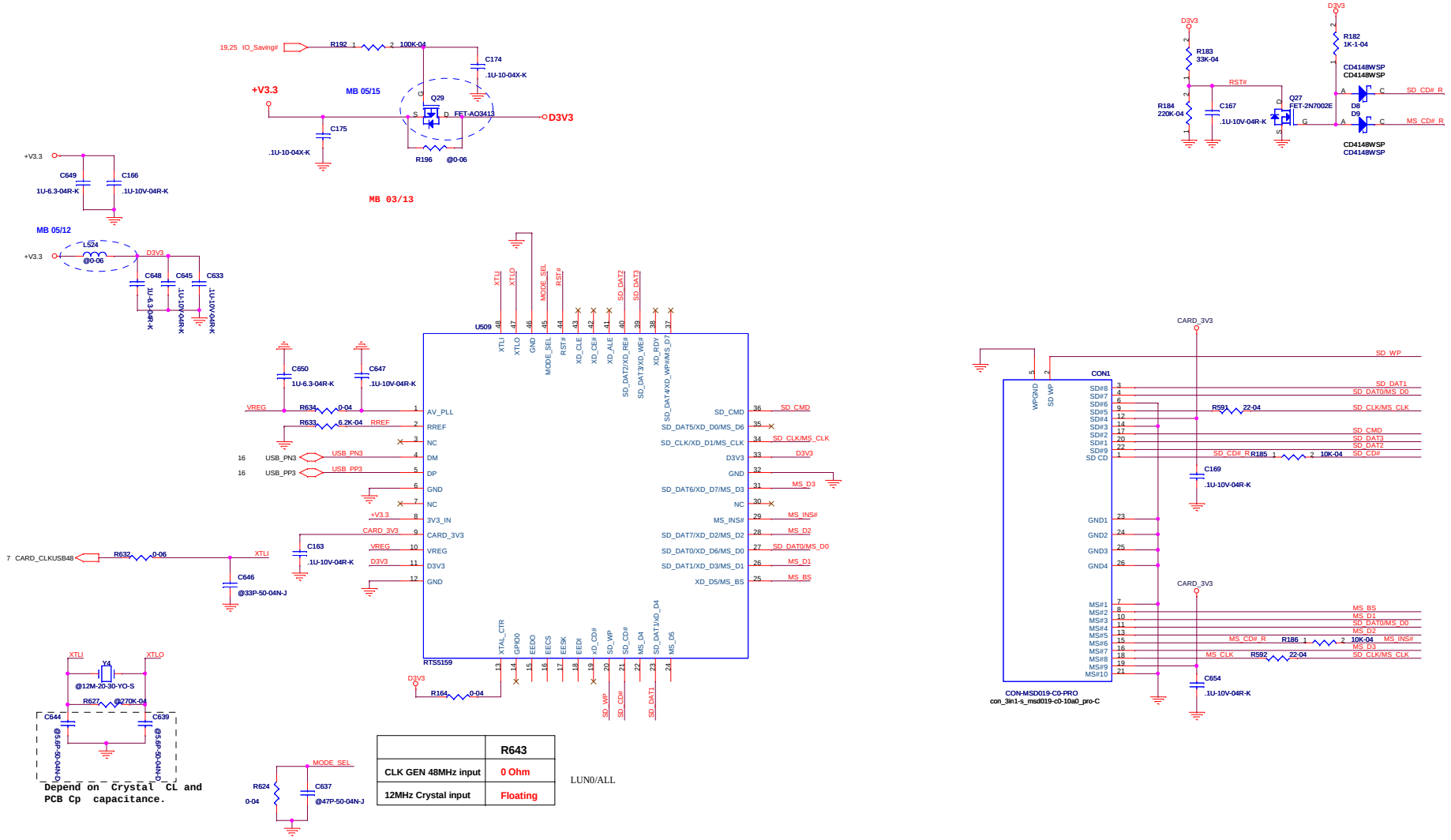


	R643
CLK GEN 48MHz input	0 Ohm
12MHz Crystal input	Floating

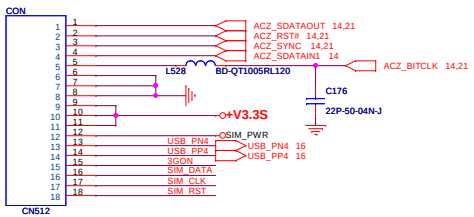
ELITEGROUP

Project : V10IL1

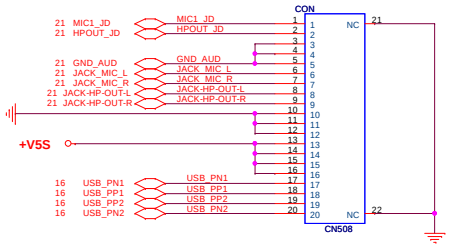
Size	Document Number	Rev
	CardReader RTS5159	B
	SCHEMATICS	

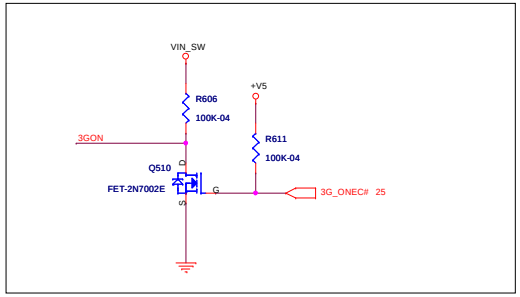
Date: Tuesday, May 19, 2009 Sheet 22 of 32

3G & MODEM CONN TO SMALL BD

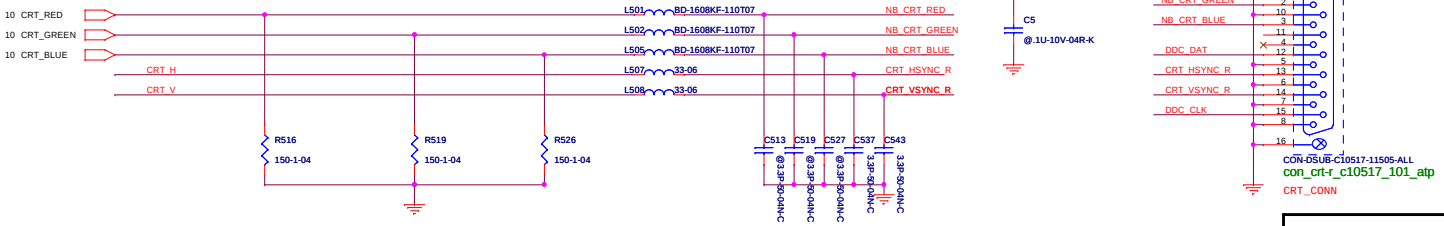


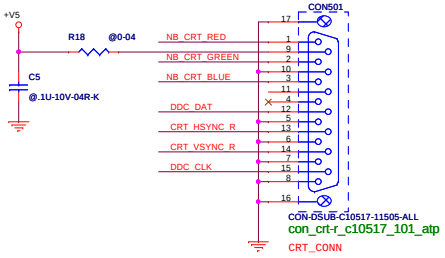
USB AUDIO RJ-11 CONNECT TO SMALL BD

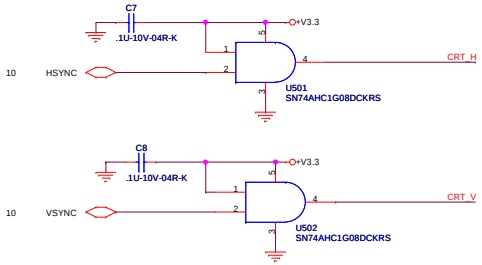


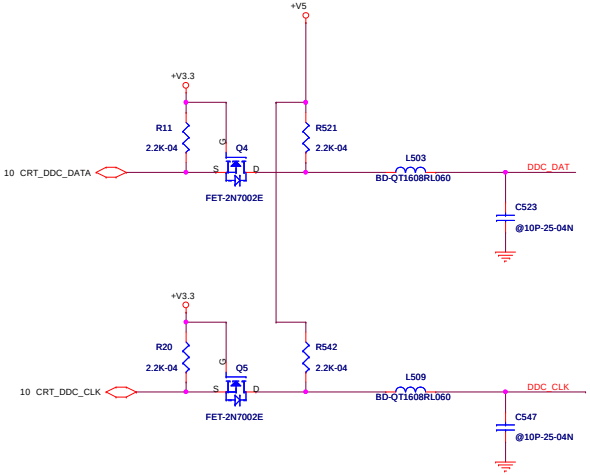


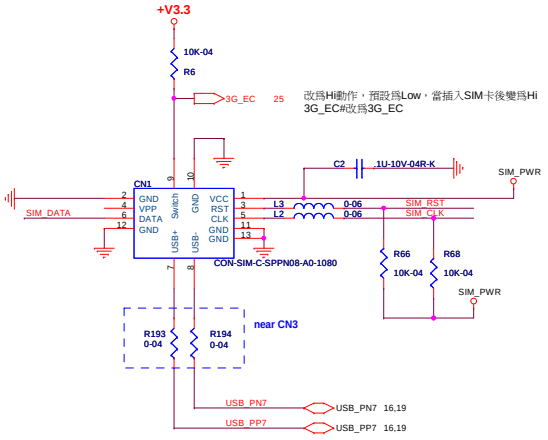
CRT









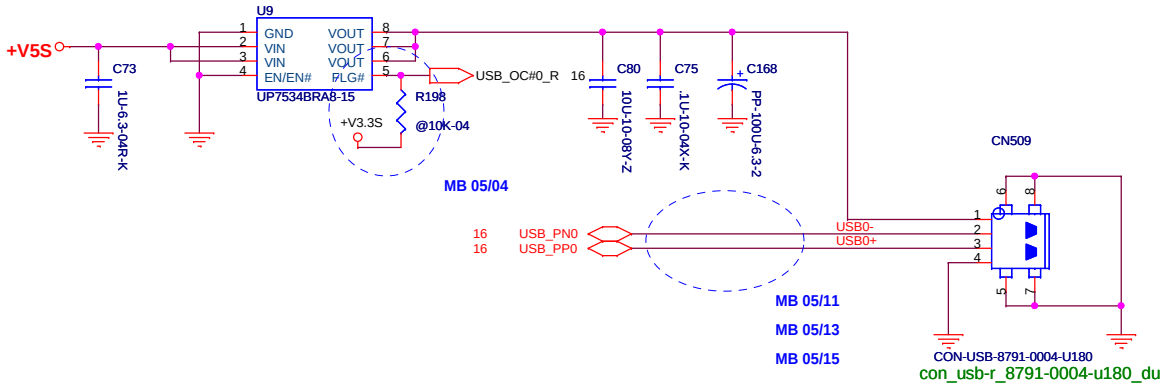


ELITEGROUP

Project : V10IL1

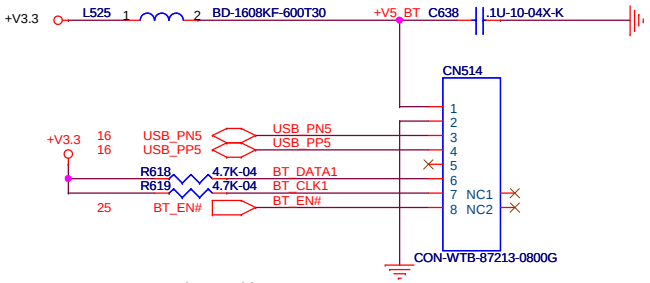
Size	Document Number	Rev
	CRT/3G TO SMAII BD/SIM CARD	B
	SCHEMATICS	
Date: Tuesday, May 19, 2009	Sheet 23 of 32	

USB Port



CO-Layout

BULETOOTH



BT_EN# = low Enable BT.
BT_EN = high Disable BT.

H501
holec197d115

H1
HOLEC256D130

H6
HOLEC256D130

MB 05/13

H2
HOLEC256D98

H3
holec237d146

H4
HOLEC256D98

H7
HOLEC256D98

H8
HOLEC256D98

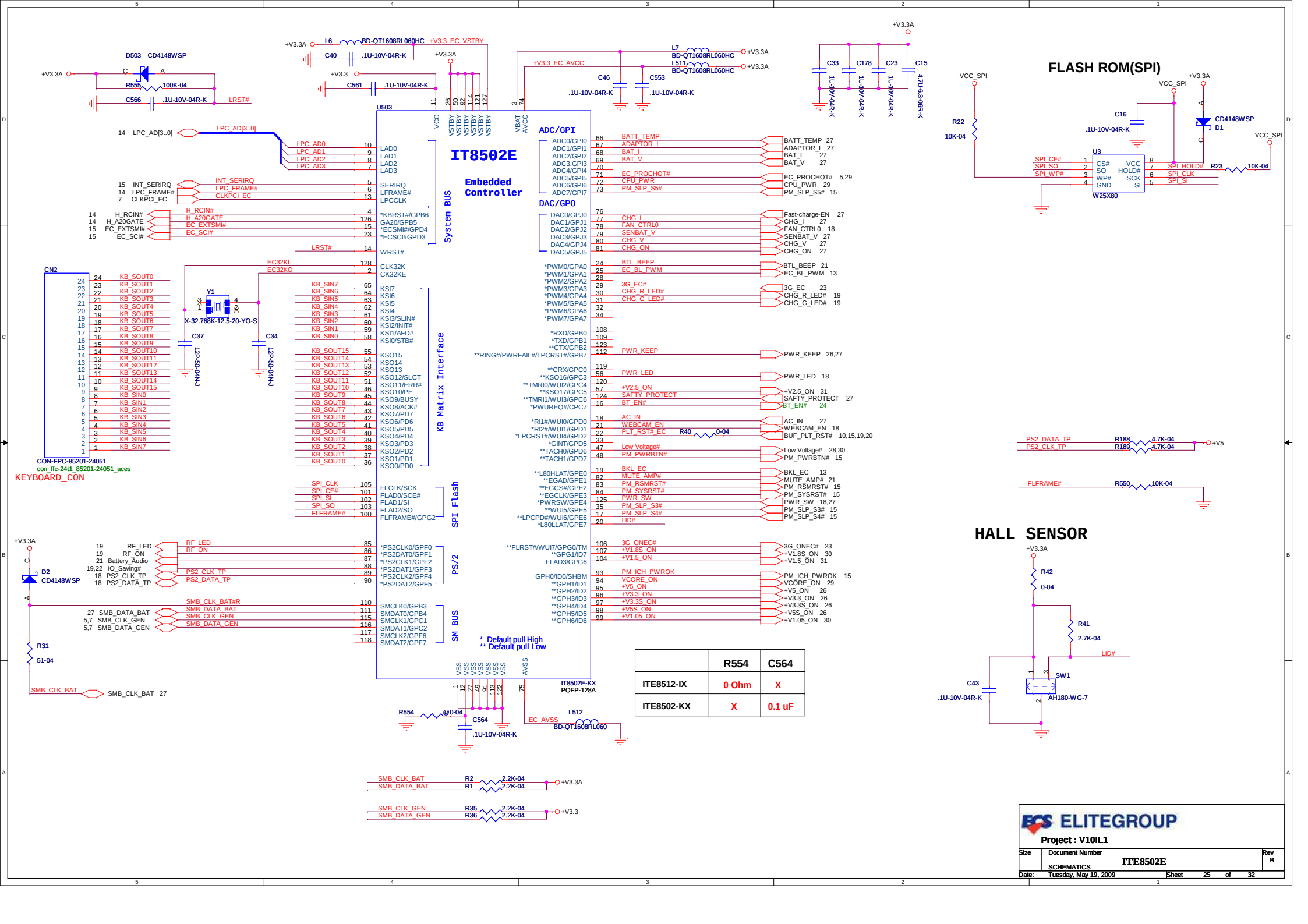
G-SENSOR

B Phase移除 G-SENSOR FUNCTION

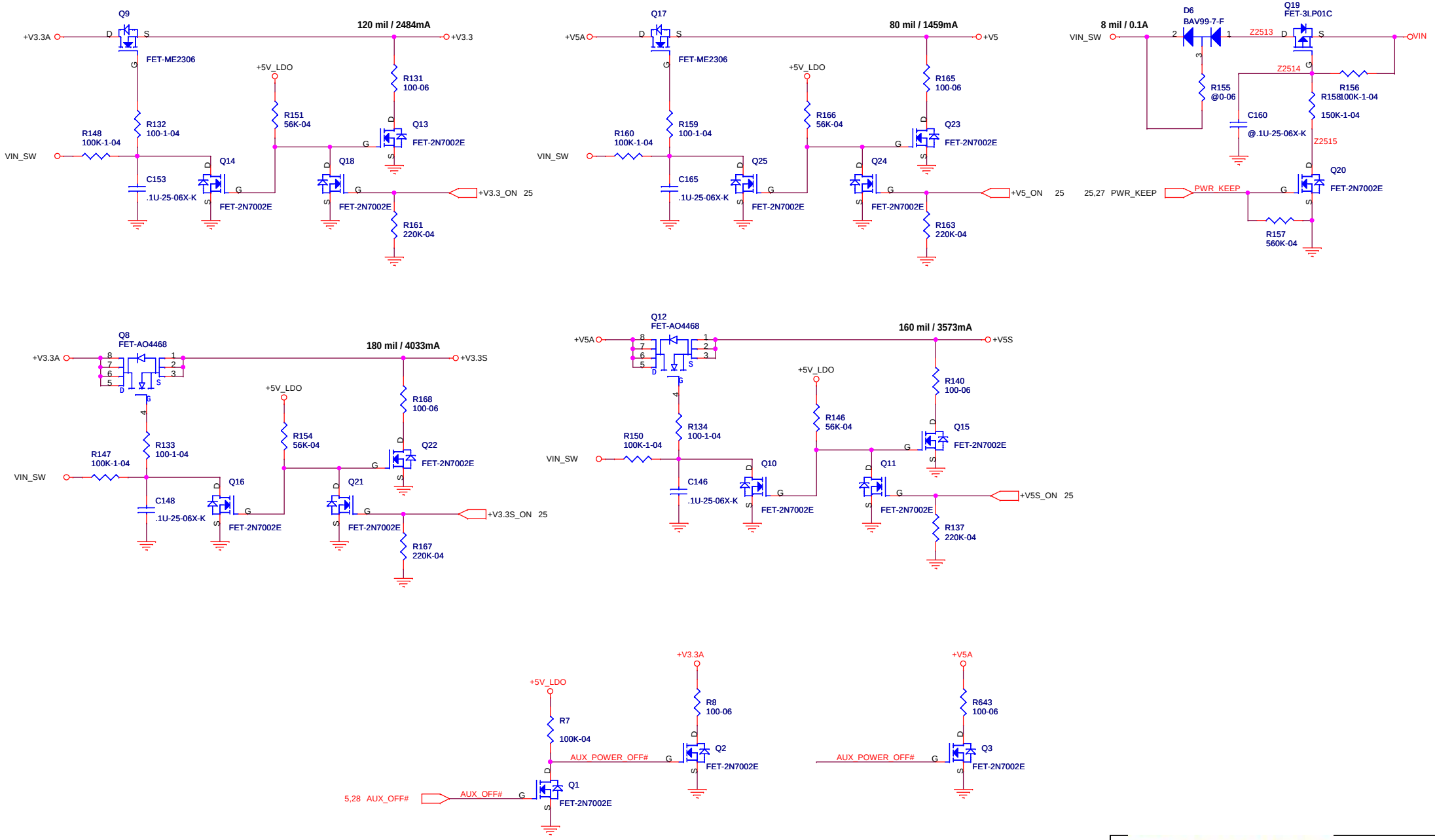
ECS ELITEGROUP

Project : V10IL1

Size	Document Number	Rev
	USB CONN/G-SENSOR/BULETOOTH	B
	SCHEMATICS	
Date:	Tuesday, May 19, 2009	Sheet 24 of 32



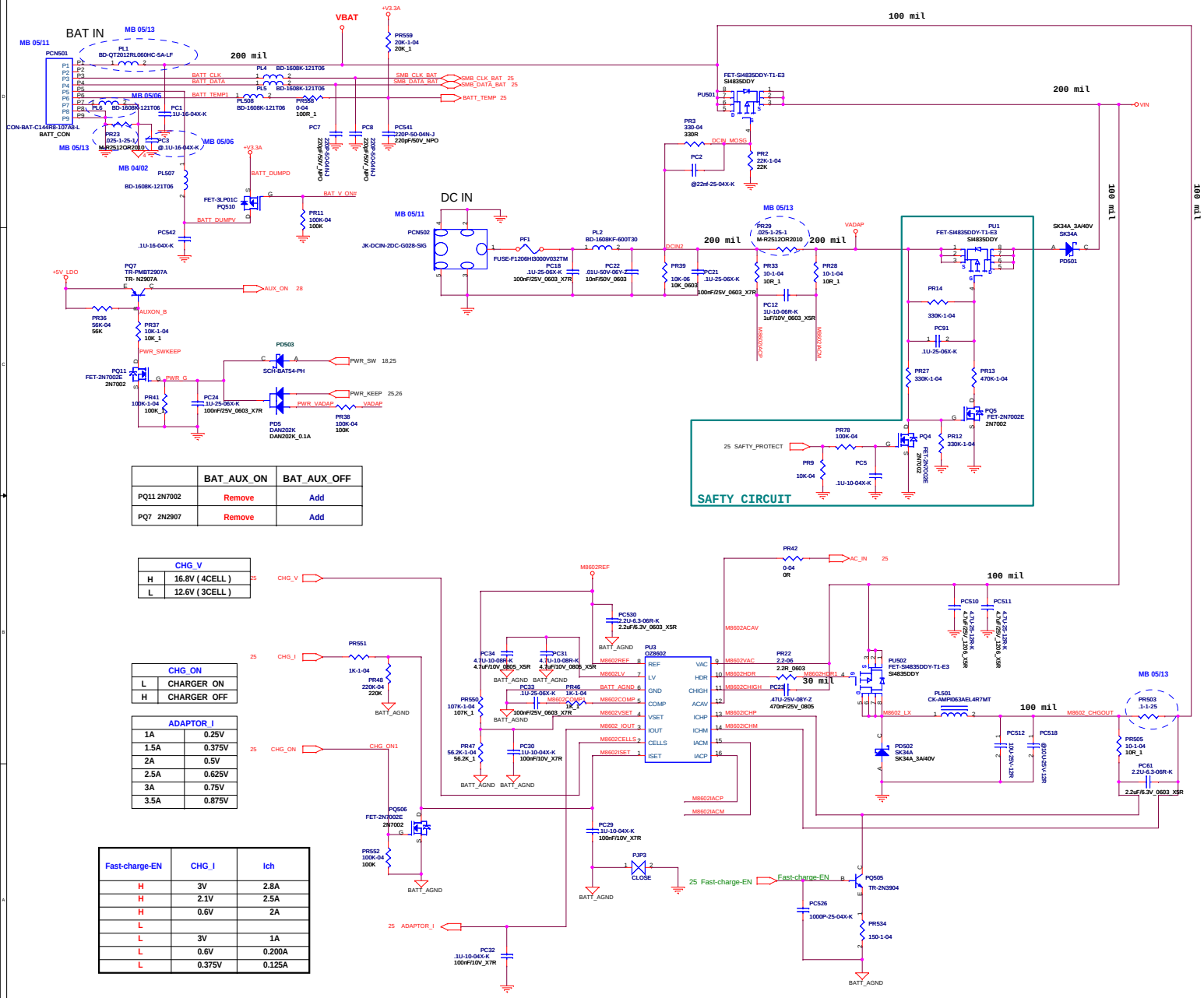
	R554	C564
ITE8512-IX	0 Ohm	X
ITE8502-KX	X	0.1 uF



ELITEGROUP

Project : V10IL1

Size	Document Number	Rev
	POWER SWITCH	B
Date: Tuesday, May 19, 2009	Sheet 26 of 32	



	BAT_AUX_ON	BAT_AUX_OFF
PQ11 2N7002	Remove	Add
PQ7 2N2907	Remove	Add

CHG_V	
H	16.8V (4CELL)
L	12.6V (3CELL)

CHG_ON	
L	CHARGER ON
H	CHARGER OFF

ADAPTOR_I	
1A	0.25V
1.5A	0.375V
2A	0.5V
2.5A	0.625V
3A	0.75V
3.5A	0.875V

Fast-charge-EN	CHG_I	Ich
H	3V	2.8A
H	2.1V	2.5A
H	0.6V	2A
L		
L	3V	1A
L	0.6V	0.200A
L	0.375V	0.125A

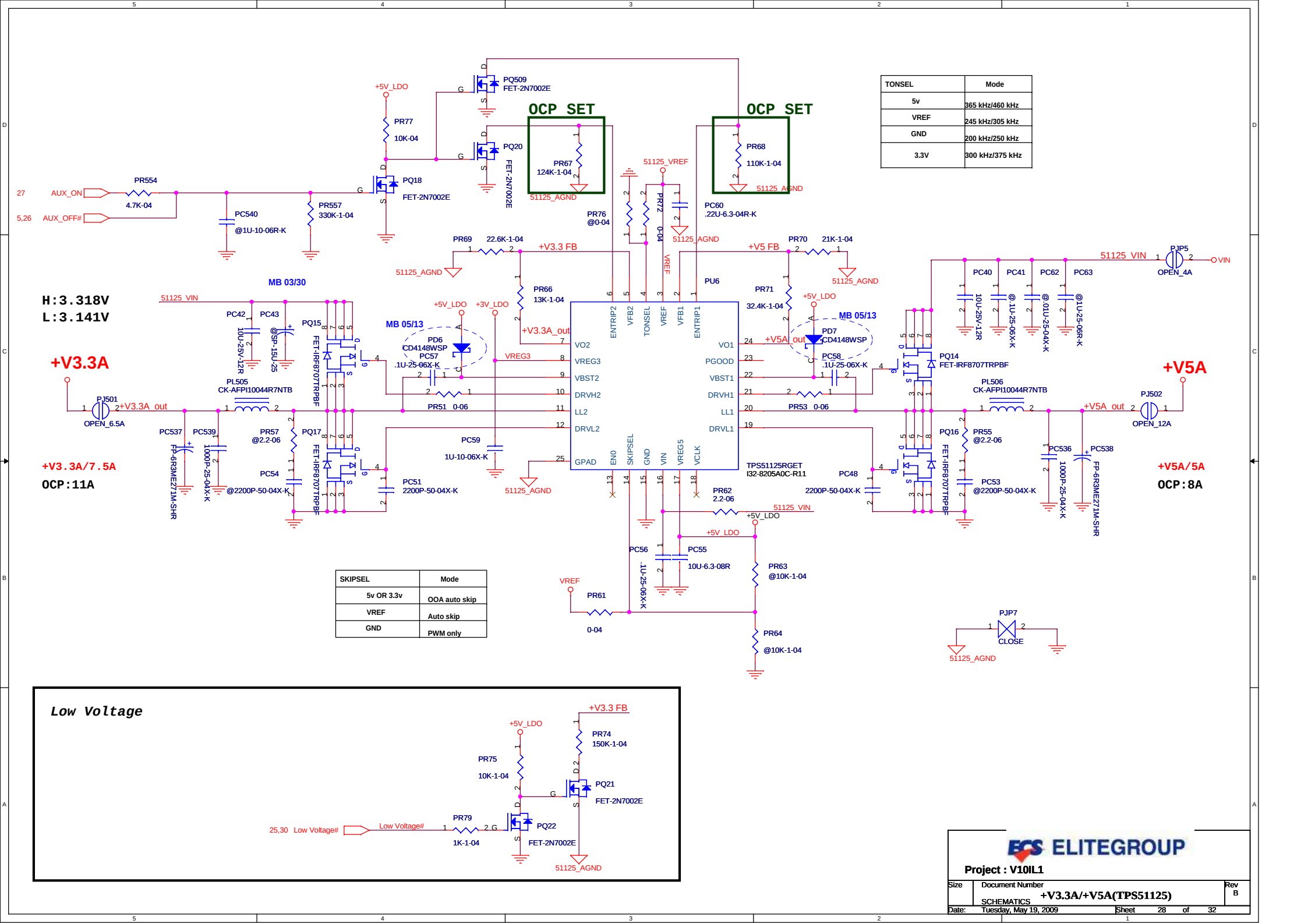
Battery Voltage Detect

Charge / Discharge Detect

ELITEGROUP

Project : V10IL1

Size	Document Number	Rev
	SCHEMATICS DC IN/BATT IN/Charger	B
Date: Thursday, May 21, 2009	Sheet 27 of 32	


TONSEL | Mode
5v | 365 kHz/460 kHz
VREF | 245 kHz/305 kHz
GND | 200 kHz/250 kHz
3.3V | 300 kHz/375 kHz
OCP SET
OCP SET
PQ509 FET-2N7002E
PR77 10K-04
PQ20 FET-2N7002E
PR67 124K-1-04
PR68 110K-1-04
PR554 4.7K-04
PQ18 FET-2N7002E
PR557 330K-1-04
PC540 @1U-10-06R-K
PC60 .22U-6.3-04R-K
PR72 0-04
PR76 @0-04
PR69 22.6K-1-04
PR66 13K-1-04
PR70 21K-1-04
PR71 32.4K-1-04
PU6
TPS51125RGET
I32-8205A0C-R11
H:3.318V
L:3.141V
MB 03/30
MB 05/13
MB 05/13
+3.3A
+V3.3A/7.5A
OCP:11A
+V5A
+V5A/5A
OCP:8A
PJ501 OPEN_6.5A
PJ502 OPEN_12A
PJP5 OPEN_4A
PJP7 CLOSE
PL505 CK-AFPI10044R7NTB
PL506 CK-AFPI10044R7NTB
PQ15 FET-IRF8707TRPBF
PQ14 FET-IRF8707TRPBF
PQ17 FET-IRF8707TRPBF
PQ16 FET-IRF8707TRPBF
PD6 CD4148WSP
PD7 CD4148WSP
PC57 .1U-25-06X-K
PC58 .1U-25-06X-K
PR51 0-06
PR53 0-06
PR57 @2.2-06
PR55 @2.2-06
PC54 @2200P-50-04X-K
PC53 @2200P-50-04X-K
PC51 2200P-50-04X-K
PC48 2200P-50-04X-K
PC59 1U-10-06X-K
PR62 2.2-06
PC56 .1U-25-06X-K
PC55 10U-6.3-08R
PR63 @10K-1-04
PR61 0-04
PR64 @10K-1-04
SKIPSEL | Mode
5v OR 3.3v | OOA auto skip
VREF | Auto skip
GND | PWM only
Low Voltage
PR74 150K-1-04
PR75 10K-1-04
PQ21 FET-2N7002E
PR79 1K-1-04
PQ22 FET-2N7002E
25,30 Low Voltage#
ELITEGROUP
Project : V10IL1
Size
Document Number
+V3.3A/+V5A(TPS51125)
Rev B
SCHEMATICS
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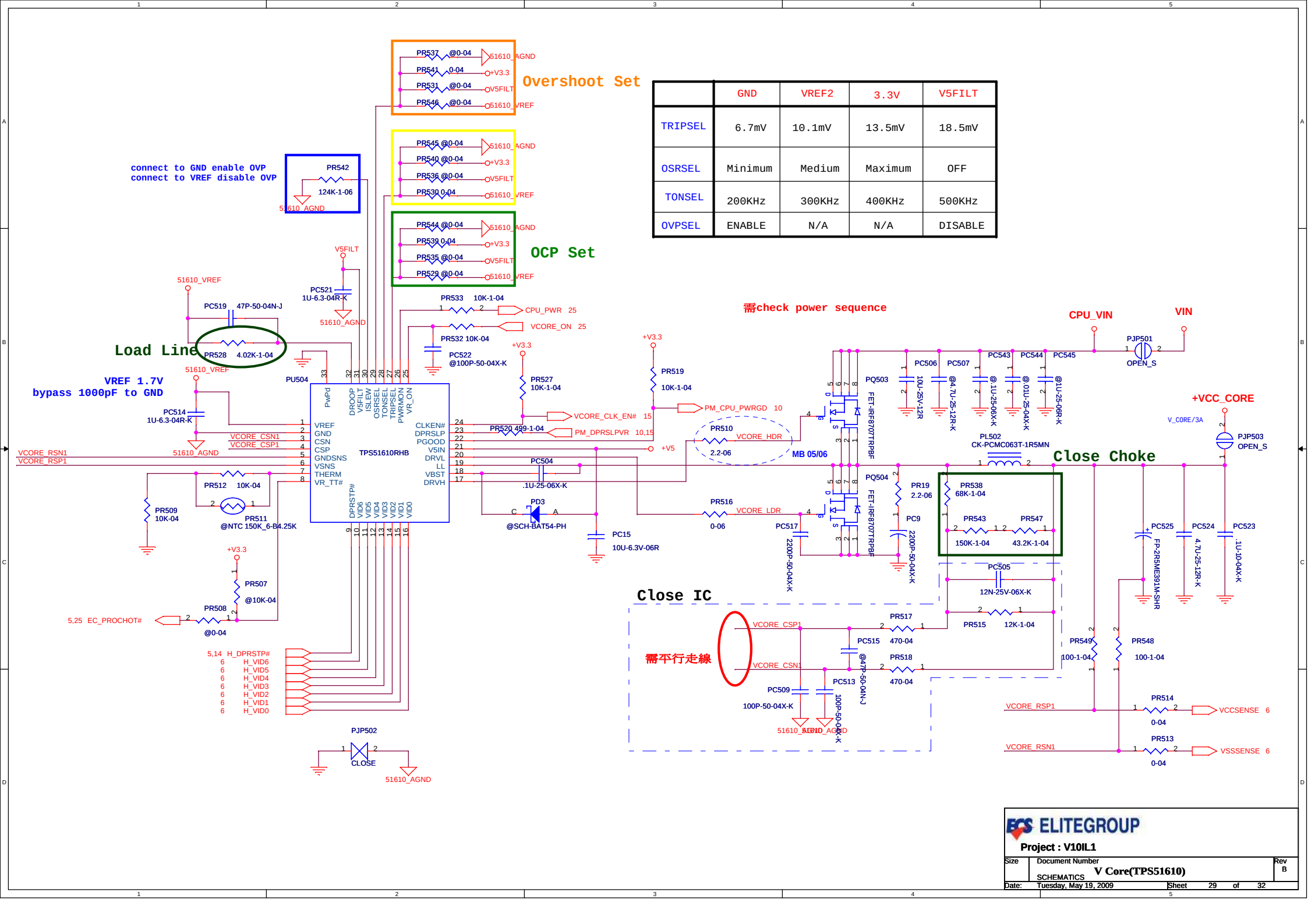
Sheet 28 of 32



	GND	VREF2	3.3V	V5FILT
TRIPSEL	6.7mV	10.1mV	13.5mV	18.5mV
OSRSEL	Minimum	Medium	Maximum	OFF
TONSEL	200KHz	300KHz	400KHz	500KHz
OVPSEL	ENABLE	N/A	N/A	DISABLE

ELITEGROUP

Project : V10IL1

Size	Document Number	Rev
	V Core(TPS51610)	B
	SCHEMATICS	
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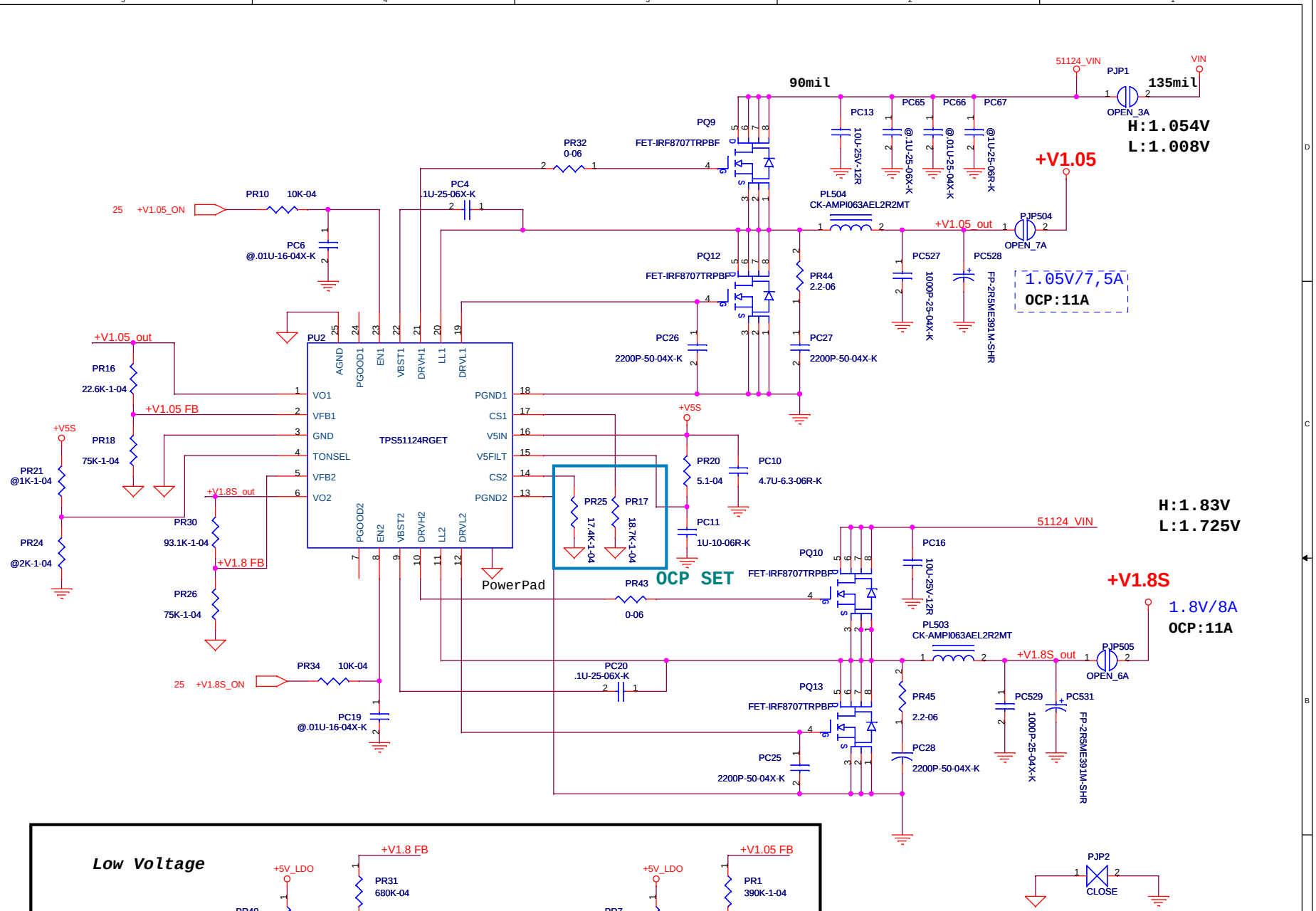

ELITEGROUP

Project : V10IL1

Size	Document Number	Rev
	+V1.05 (OZ8116)	B
	SCHEMATICS	
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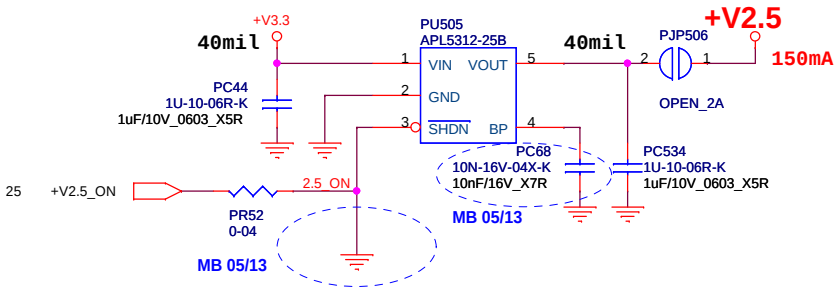
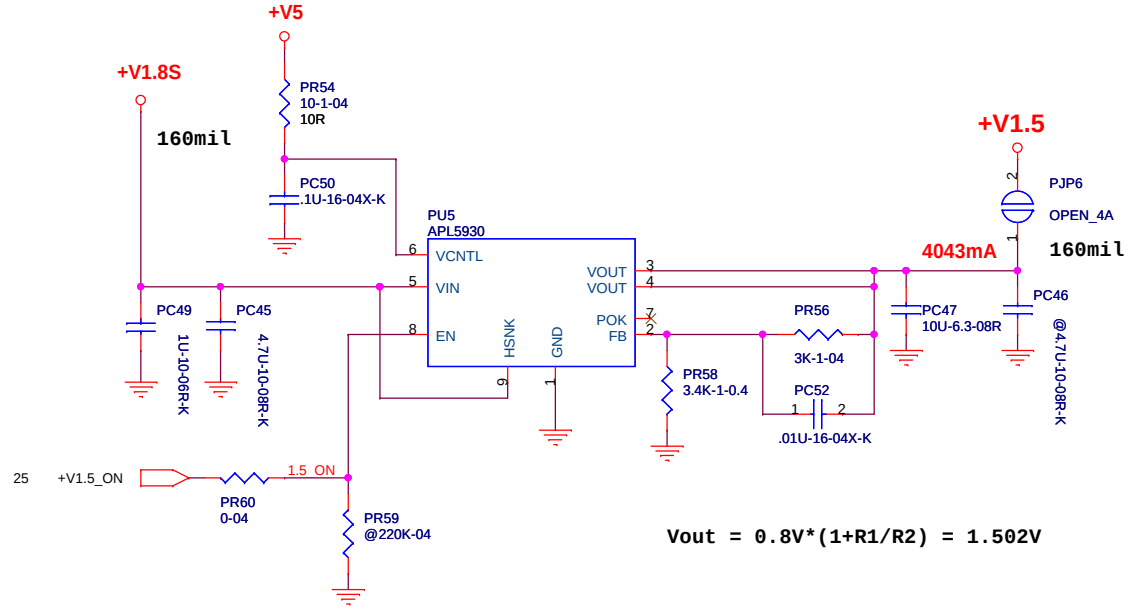
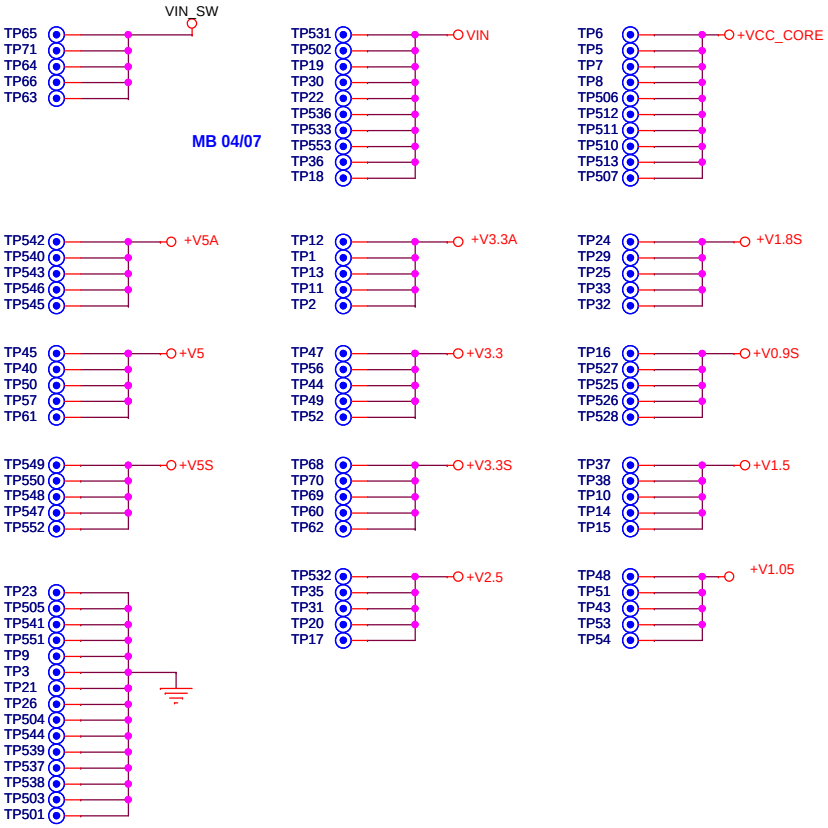
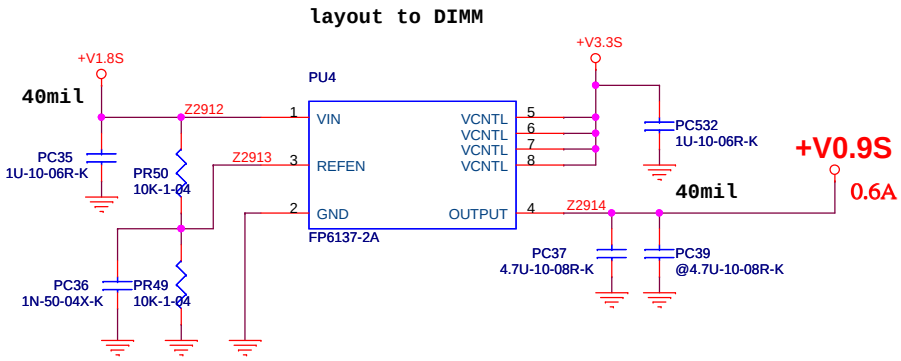




POWER TP





ELITEGROUP

Project : V10IL1

Size	Document Number	Rev
	+V1.5/+V0.9S/+V2.5/+V2.5S	B
	SCHEMATICS	
Date: Tuesday, May 19, 2009	Sheet 31 of 32	

V10IL1 C phase modify list:

MB 04/28

1. page 16 新增電容C177，1000P-25-04X-K
2. page 03 修改POWER SEQ

MB 05/04

1. page 16 新增R197 0OHM 0402，上件
2. page 24 新增R198 10K OHM 0402 PULL HI +V3.3S，不上件

MB 05/06

1. page 29 PR510改阻值爲2.2 ohm
2. page 27 新增PL6 BD-1608K-121T06，上件
   新增PC3 @.1U-16-04X-K，不上件

MB 05/11

1. page 25 C558改C178
2. page 24 移除L516
3. page 20 移除L8
4. Q507、Q1、Q9、Q17零件的Footprint：M-SOT23G改爲M-SOT23G-1
5. PCN502的Footprint改爲2DC-G028X200
6. PCN501的Footprint改爲CON-BAT-C144G9-ALL

MB 05/12

1. page 22 L524改不上件

MB 05/13

1. page 23 H3改holec237d146
2. page 28 PD6、PD7上件
3. page 31 移除R553
4. page 13 移除L519
5. page 24移除R576、R577
6. page 20 L8先加回，等EMI通知
7. page 27 PL1的Footprint改爲M-R0805
8. page 27 PR503、PR23、PR29的Footprint改爲M-R2512OR2010
9. page 13 PC535改名爲PC68
10. page 31 R587改名爲R199

MB 05/14

1. page 13 R199改名爲R587
2. Q1零件的Footprint改回M-SOT23G
3. page18 C651改名C179
   C652改名C180

MB 05/15

1. page 18 SW2、SW3的Footprint改爲con_sw-t_ntc010-dc1g-e160t_tmec
2. page 23 CN1的Footprint改爲CON_SIM_SPPN08-A0-1030_PRO
3. Q6、Q28、Q29改爲FET-AO3413，Footprint改爲M-SOT23G_B

MB 05/16

1. page 19 R169、R171、R172、R175、R176改爲220-04
   R173、R174、R177、R178、R179改爲220-04此爲For ID2的零件，ID1不上件
2. page 18 R508改爲2.2K-04
3. page 18 因爲SW2、SW3的Footprint改爲con_sw-t_ntc010-dc1g-e160t_tmec
   所以pin name也跟著做修正，A改1、B改3、C改2、D改4、E改5

MB 05/18

1. page 14 JP1更名爲JP502，並更換位置，解決無法clear CMOS的問題
2. page 20 RP1移除，L8改上件

Elitegroup Computer Systems		
Title: Change Notes		
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